

A Review of Three-Phase Improved Power Quality AC–DC Converters

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Abstract—Three-phase ac–dc converters have been developed to a matured level with improved power quality in terms of power-factor correction, reduced total harmonic distortion at input ac mains, and regulated dc output in buck, boost, buck–boost, multilevel, and multipulse modes with unidirectional and bidirectional power flow. This paper presents an exhaustive review of three-phase improved power quality ac–dc converters (IPQCs) configurations, control strategies, selection of components, comparative factors, recent trends, their suitability, and selection for specific applications. It is aimed at presenting a state of the art on the IPQC technology to researchers, designers, and application engineers dealing with three-phase ac–dc converters. A classified list of around 450 research articles on IPQCs is also appended for a quick reference.

Index Terms—Harmonic reduction, improved power quality, power-factor correction, switch-mode rectifiers, three-phase ac–dc converters.

I. INTRODUCTION

THREE-PHASE ac–dc conversion of electric power is widely employed in adjustable-speeds drive (ASDs), uninterruptible power supplies (UPSs), HVdc systems, and utility interfaces with nonconventional energy sources such as solar photovoltaic systems (PVs), etc., battery energy storage systems (BESSs), in process technology such as electroplating, welding units, etc., battery charging for electric vehicles, and power supplies for telecommunication systems [1]–[25]. Traditionally, ac–dc converters, which are also known as rectifiers, are developed using diodes and thyristors to provide controlled and uncontrolled unidirectional and bidirectional dc power. They have the problems of poor power quality in terms of injected current harmonics, resultant voltage distortion and poor power factor at input ac mains and slowly varying rippled dc output at load end, low efficiency, and large size of ac and dc filters. In view of their increased applications, a new breed of rectifiers has been developed using new solid-state self-commutating devices such as MOSFETs, insulated gate bipolar transistors (IGBTs),

gate-turn-off thyristors (GTOs), etc. Such converters are generally classified as switch-mode rectifiers (SMRs), power-factor correctors (PFCs), pulsewidth-modulation (PWM) rectifiers, multilevel rectifiers, multipulse rectifiers, etc. Because of the strict requirement of power quality at the input ac mains, several standards [1]–[3] have been developed and enforced on the consumers. Because of the severity of power quality problems some other options such as passive filters, active filters (AFs), and hybrid filters [4], [7]–[9] along with conventional rectifiers have been extensively developed, especially in large rating and already existing installations. However, these filters are quite costly, bulky, and have reasonable losses, which reduce overall efficiency of the complete system. Even in some cases the rating of converter used in active filters is almost close to the rating of the load. Under such circumstances, it is considered better option to use such converters as an inherent part of the system of AC-DC conversion, which provides reduced size, high efficiency, and well controlled and regulated DC to provide comfortable and flexible operation of the system. Moreover, these new types of AC-DC converters are being included in the new textbooks and several comparative topologies are reported in recent publications [10]–[17]. Therefore, it is considered a timely attempt to present a broad perspective on the status of ac–dc converters technology for the engineers using them and dealing with power quality issues.

This paper presents a comprehensive survey on three-phase ac–dc converters. More than 450 publications [1]–[477] are reviewed and classified into three major categories. Some of them are further classified into several subcategories. The first one [1]–[25] is general on power quality standards, other options, texts, and some surveys and comparative topology publications. The second and third categories are on unidirectional and bidirectional power flow ac–dc converters [26]–[477]. These converters are further subclassified as boost [26]–[245], buck [246]–[347], buck–boost [348]–[383], multilevel [384]–[430], and multipulse ac–dc converters [431]–[477]. The total number of configurations of these converters is divided into ten categories. The paper is divided into nine parts. Starting with the introduction (Section I), other sections cover the state of the art of IPQC technology (Section II), configurations (Section III), control strategies (Section IV), components selection for IPQCs (Section V), comparative factors and others options for power quality improvement (Section VI), selection considerations of IPQCs for specific applications by the designers (Section VII), latest trends and future developments in IPQC technology (Section VIII), and a conclusion (Section IX).

Manuscript received July 15, 2002; revised June 23, 2003. Abstract published on the Internet January 14, 2004. This work was supported by the Natural Sciences and Engineering Research Council of Canada.

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Digital Object Identifier 10.1109/TIE.2004.825341

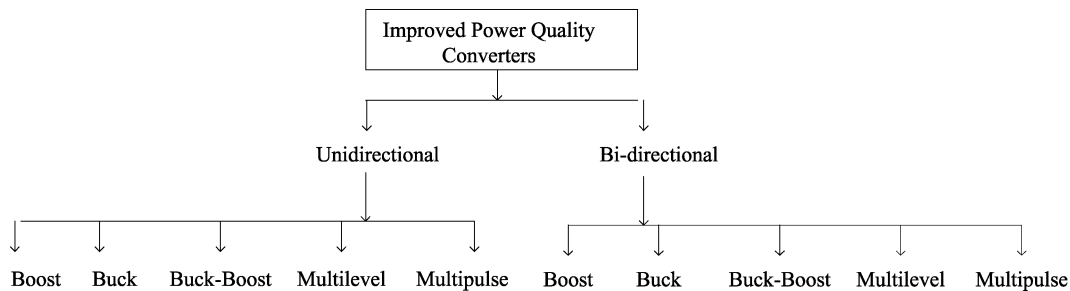


Fig. 1. Converter-based classification of improved power quality converters.

II. STATE OF THE ART

IPQC technology is matured at a reasonable level for ac–dc conversion with reduced harmonic currents, high power factor, low electromagnetic interference (EMI) and radio frequency interference (RFI) at input ac mains and well-regulated and good quality dc output to feed loads ranging from fraction of kilowatt to megawatt power ratings in a large number of applications. These were developed in the last couple of decades with varying configurations, control strategies, solid-state devices, circuit integration, varying magnetics in topologies such as boost, buck, buck–boost, and multilevel for unidirectional and bidirectional power flow. A large number of IPQC configurations have been evolved to suit vastly varying applications while maintaining a high level of quality at input ac source and output dc loads.

In some applications, a constant-regulated output dc voltage is required with unidirectional power flow such as in UPSs, ASDs in fans, air conditioners, etc., while in some other applications, a bidirectional power flow is required. Therefore, these IPQCs are categorized into unidirectional boost converters [26]–[86] and bidirectional boost converters [87]–[245]. Moreover, there are a number of applications which require widely varying dc voltage, normally fed from a conventional semiconverter or fully controlled thyristor converter with unidirectional or bidirectional power flow. To replace the conventional thyristor-based semi and full converters, a breed of improved power quality converters has been developed and classified as unidirectional buck [246]–[263] and bidirectional buck converters [264]–[347] using PWM switching with self-commutating solid-state devices. Moreover, there are some typical applications which require buck and boost operations in the same converter, therefore, an additional classification of buck–boost converters is made with unidirectional [348]–[373] and bidirectional power flow [374]–[383]. However, for high-voltage and high-power applications, the concept of multilevel converters is developed which may avoid a low-frequency transformer and reduces the switching frequency of the devices [384]–[430]. Therefore, the next category of IPQC is considered as multilevel converters with unidirectional [384]–[400] and bidirectional power flow [401]–[430].

In high-power applications, ac–dc converters based on the concept of multipulse, namely, 12, 18, 24, 30, 36, 48... pulses are used to reduce the harmonics in ac supply currents. These are named as multipulse converters [431]–[477]. They use either a diode bridge or thyristor bridge and a special arrangement of magnetics through transformers and tapped inductors. Therefore, the last category is multipulse converters with unidirectional [431]–[455] and bidirectional power flow [456]–[477].

One of the important reasons for such an extensive development in ac–dc converters is due to self-commutating devices. At low power rating, MOSFETs are used with unsurpassed performance because of their high switching rate with negligible losses. At medium power rating, an IGBT is considered an ideal device for such converters with PWM technology. At a higher power rating, a GTO is normally used with self-commutating and reverse voltage-blocking capabilities at only a few kilohertz switching frequency. A number of manufacturers are developing an intelligent power module (IPM) with several devices to give a cost effectiveness and compact size to the IPQCs. Another breakthrough in IPQCs has been because of fast response Hall-effect voltage and current sensors, and isolation amplifiers normally required for the feedback used in the control of these ac–dc converters result in a high level of dynamic and steady-state performance. Many manufacturers, such as ABB, LEM, HEME, Analog Devices, and others are offering the sensors at competitively low prices.

A major boost to the technology of IPQCs has also been due to the revolution in microelectronics. Because of the heavy volume requirement, a number of manufacturers have developed dedicated ICs for cost-effective and compact control of these converters. Moreover, high-speed microcontrollers and digital signal processors (DSPs) are available at reasonable cost. Many processors have been developed to give direct PWM outputs with fast software algorithms such as space-vector control (SVC) [36], [39], [47], [66], [103], [129], [152], [168], [215], [369], [417], normally used in some of these converters, which reduce hardware drastically. With these processors it is now possible to implement new and improved control algorithms to provide fast dynamic performance of IPQCs. Starting with proportional–integral (PI) controllers, sliding-mode, fuzzy logic, and neural-network-based controllers have been employed for the control of these converters. Moreover, a number of instruments are available to measure the performance of these IPQCs, which are named as power analyzers, power scopes, power monitors, and spectrum analyzers. They provide direct harmonic spectrum, total harmonic distortion (THD) even up to 51st order of harmonics, power factor, crest factor, displacement factor, kVA, kVAr, kW and kWh, ripples, surge, swell, notch width, and height.

III. CONFIGURATIONS OF IPQCS

IPQCs are classified into ten categories on the basis of converter circuit topologies such as buck, boost, buck–boost, multilevel, and multipulse, with unidirectional and bidirectional dc output voltage, current, and power flow. Fig. 1 shows the tree of

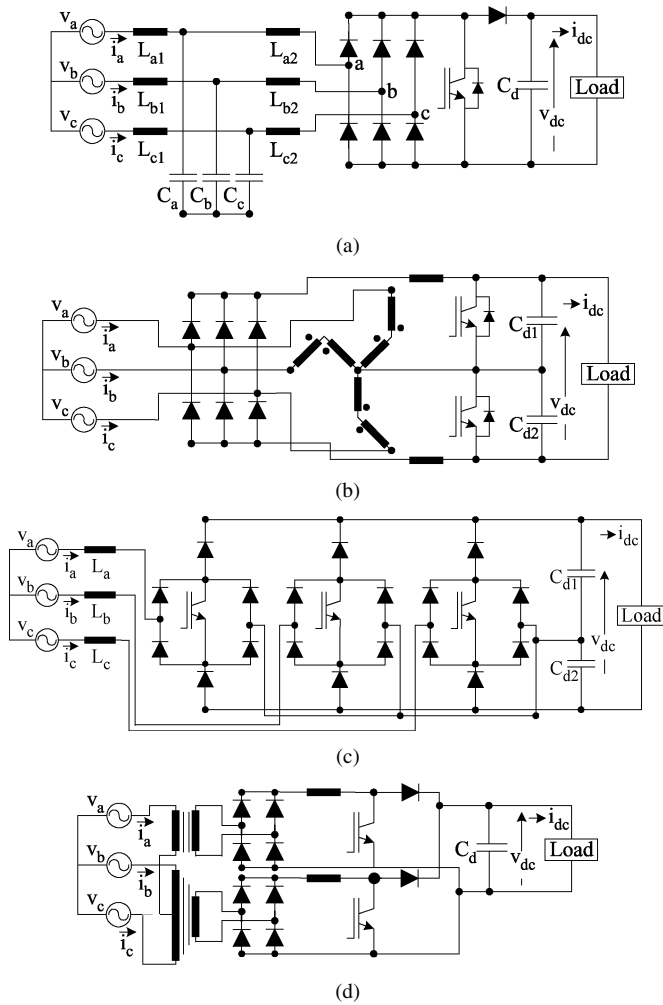


Fig. 2. (a) Single-switch unidirectional boost converter. (b) Two-switch unidirectional boost converter using zigzag injection transformer (Minnesota rectifier). (c) Three-switch unidirectional boost converter (Vienna rectifier). (d) Unidirectional boost converter using isolated Scott connection transformers.

such classification of IPQCs. These converters are developed in such vastly varying configurations as to fulfill the very close and exact requirement in a variety of applications. Figs. 2–11 show basic circuit configurations of three-phase IPQCs of all ten categories for ac–dc conversion.

A. Unidirectional Boost Converters [26]–[86]

These types of converters are widely used nowadays as a replacement of a conventional diode rectifier to provide unity power factor, reduced THD at ac mains, and constant-regulated dc output voltage even under fluctuations of ac voltage and dc load. Fig. 2 shows the few circuits of this category of converters. There is wide variety of configurations with single-switch, two-switch, three-switch, etc., to improve their performance toward ideal power quality conditions at ac input mains and dc output. Single-switch with passive filter [Fig. 2(a)] [26]–[28], Minnesota rectifier [Fig. 2(b)] using harmonic current injection through a zigzag transformer [27], [29], Vienna rectifier [Fig. 2(c)] [41], [72], [78], [84], [86], and isolated Scott-connected transformer with dual-boost PFC [Fig. 2(d)] [25] are a few pioneer configurations of these types

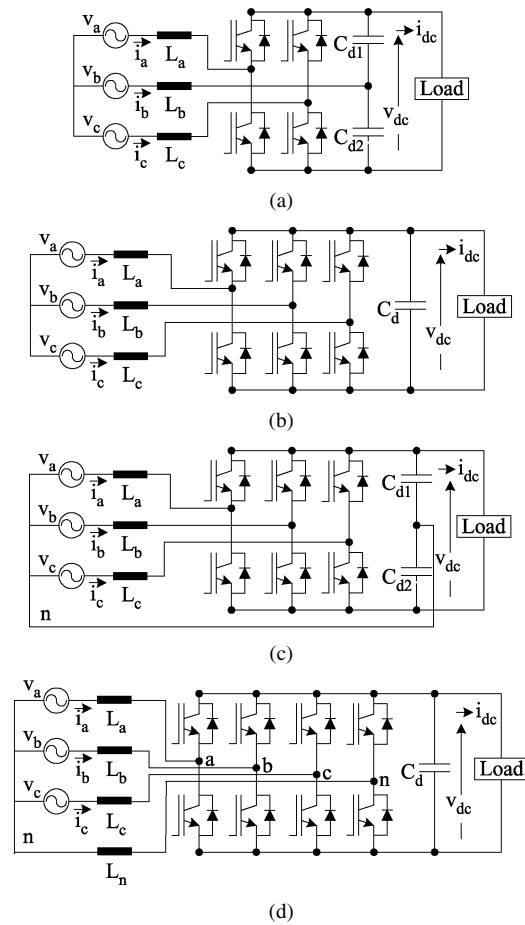


Fig. 3. (a) Four-switch bidirectional boost converter. (b) VSI-bridge-based bidirectional boost converter. (c) Four-wire bidirectional boost converter. (d) Four-legged bidirectional boost converter.

of converters. However, large numbers of circuits of these converters are reported using a combination of single-phase boost converters [34] and other modified topologies and extensively used in power supplies and motor speed control.

B. Bidirectional Boost Converters [87]–[245]

For the bidirectional power flow from ac mains to dc output and vice versa, an ideal converter is normally used in hoists, cranes, lifts, BESSs, line interactive UPSs, etc. [89], [90], [92], [93]. Fig. 3 shows the few circuit diagrams of these bidirectional converters. The closed-loop control of dc-bus voltage decides the amplitude of supply currents, which are in phase with ac mains voltages. PWM current control of the voltage-source-inverter (VSI)-based converter maintains the ac supply current close to sinusoidal and in phase with ac mains voltages.

These converters are developed using four switches to reduce the cost [Fig. 3(a)] for variable-speed induction motor drives [151]. Ideally, a six-device VSI bridge is used in the majority of cases [Fig. 3(b)] [92]. However, four-wire topologies [Fig. 3(c) and (d)] [206], [235] are employed to reduce the dc-link voltage ripple and balancing the supply currents, even in the case of unbalance supply voltages. There have been many pioneer developments in these converters, such as sensorless control to reduce cost and number of components in the hardware [119], [120], [163], [188], [211], [213], [228], [230], [233], [234], [245].

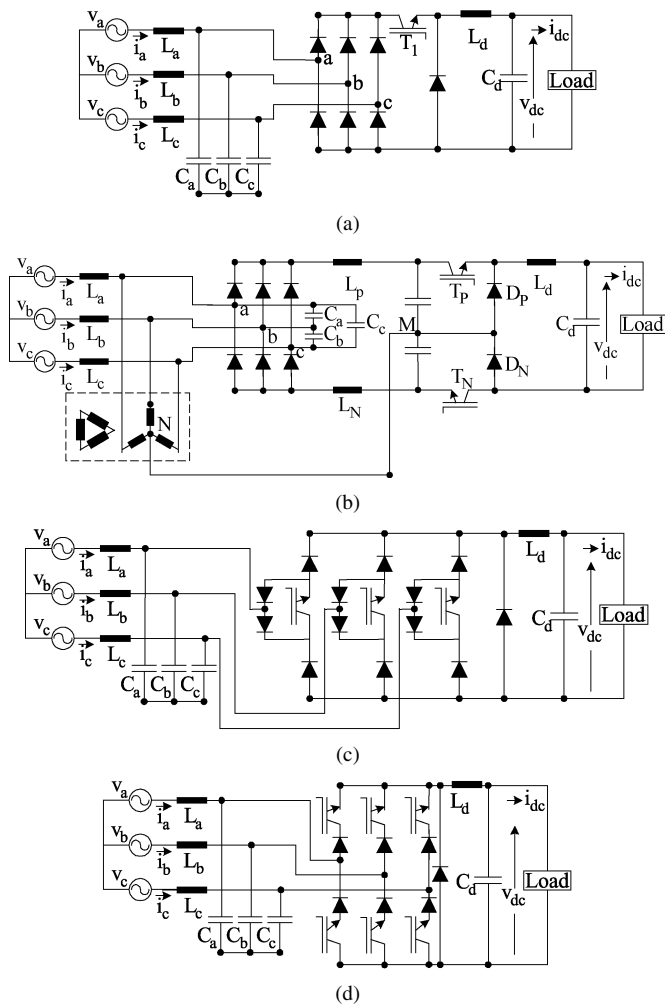


Fig. 4. (a) Single-switch unidirectional buck converter. (b) Two-switch unidirectional buck converter. (c) Three-switch unidirectional buck converter. (d) Three-phase CSI-based unidirectional buck converter.

C. Unidirectional Buck Converters [246]–[263]

This is a replacement of the thyristor semiconverter with improved power quality at ac mains and output dc bus. It provides the voltage below the base voltage. Fig. 4 shows the circuits of these converters. The requirement of the filter is normally high in this case. Several topologies, namely, using single device [Fig. 4(a)] [247], two devices [Fig. 4(b)] [251] with harmonic injection transformers, three devices with dual diode [Fig. 4(c)] [249], [259], [263], and six devices with free-wheeling diode [Fig. 4(d)] [258] are reported in the literature to improve the power factor and reduce the harmonic currents at input ac mains and well regulated filtered output dc voltage. High-frequency PWM control of switching devices reduces the size of input and output filters, weight, and enhances the efficiency of the overall system. These converters are extensively used in battery charging in automotive applications [252], and dc motor speed control in a number of applications. In these converters, the inrush currents are observed to be of low value. It is because the controlled device (IGBT) is connected in series path of the current flow. These converters are capable of giving the output dc voltage from zero to nominal values at quite a fast rate.

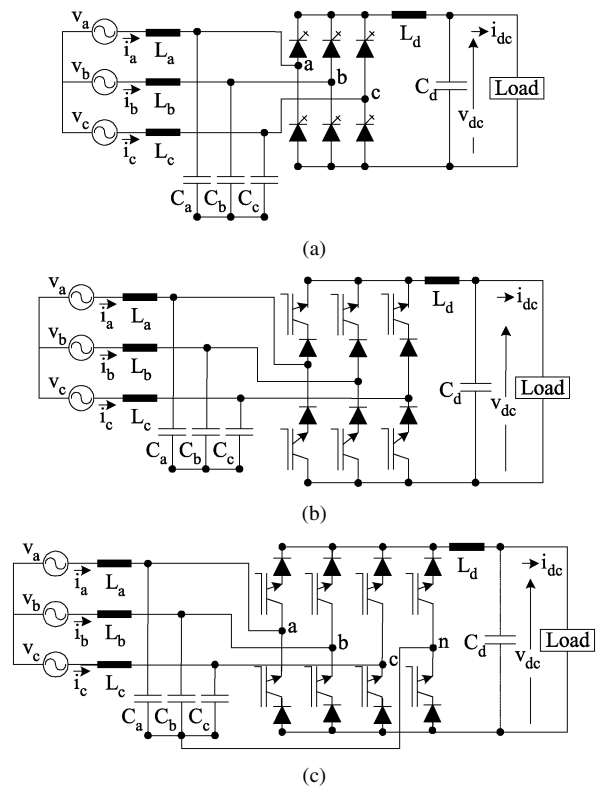


Fig. 5. (a) GTO-based bidirectional buck converter. (b) IGBT-based bidirectional buck converter. (c) Four-pole bidirectional buck converter.

D. Bidirectional Buck Converters [264]–[347]

Fig. 5 shows the circuits of such converters. It provides a similar function as a conventional thyristor bridge converter but with improved power quality in terms of high power factor and reduced harmonic currents at ac mains and fast regulated bidirectional output voltage for reversible power flow. These are developed using GTOs at higher power ratings as shown in Fig. 5(a) [271], [274] and using IGBTs with series diodes [Fig. 5(b)] [310] at low power ratings with high switching frequency, resulting in reduced size of filter components. The four-leg configuration, as shown in Fig. 5(c) [314], is implemented to reduce the output dc ripple and balanced currents under unbalance voltage of the mains. IGBTs, bipolar junction transistors (BJTs), and MOSFETs need series diodes to provide reverse voltage blocking capability required in this converter. These two bridges connected in antiparallel provide behavior similar to a dual converter for four-quadrant operation with improved power quality and fast response [278].

E. Unidirectional Buck–Boost Converters [348]–[373]

Fig. 6 shows some circuits of these converters. These are used in a wide variety of applications. They may have either isolated or nonisolated dc output from input ac mains. It consists of a combination of buck and boost converters as shown in Fig. 6(a) [259]. These converters are also realized as a combination of three-phase diode bridge with filter and buck–boost dc–dc converters such as SEPIC [Fig. 6(b)] [364], flyback [368], [371], Cuk [360], etc. For the isolated dc output with a high-frequency transformer to reduce the size, a diode rectifier in conjunction with flyback [Fig. 6(c)] [366], isolated Cuk [Fig. 6(d)] [360],

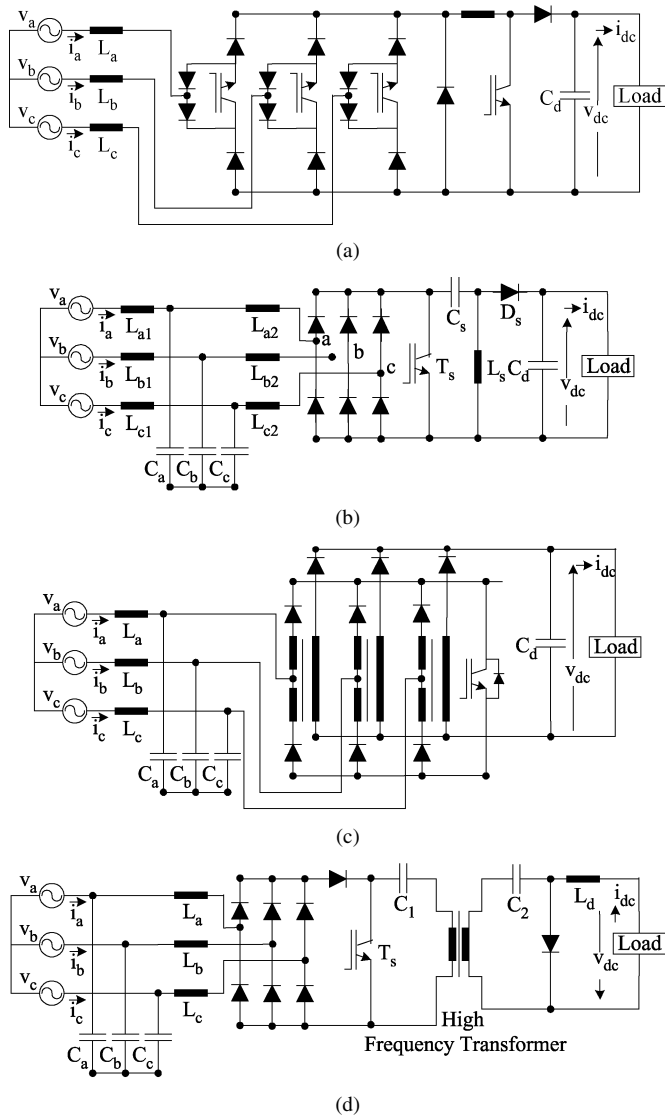


Fig. 6. (a) Four-switch unidirectional buck–boost converter. (b) SEPIC-derived unidirectional buck–boost converter. (c) Flyback-derived unidirectional buck–boost converter. (d) Isolated Cuk-derived unidirectional buck–boost converter.

and many others, such as Zeta [373], SEPIC [364], bridge, half-bridge, and push–pull are used with a first stage PFC. Nowadays, two-stage conversion is integrated together and even in single stage it is possible to achieve the same level of performance, as shown in Fig. 6(c) and (d) [366], [368] using single switch. There are such novel configurations to provide compact, integrated, high-power-density, high-efficiency power supplies for use in a number of applications such as telecommunication power supplies, battery charger units, etc.

F. Bidirectional Buck–Boost Converters [374]–[383]

There are some applications which require output dc voltage widely varying from low voltage to high voltage with bidirectional dc current as four-quadrant operation and bidirectional power flow. These converters can be implemented in many ways, such as cascading the buck and boost converters, but the simplest way of realizing them is by using a matrix converter as shown in Fig. 7 [382]. With the high-frequency switching,

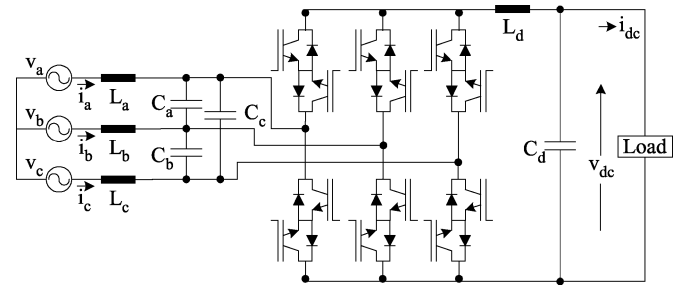


Fig. 7. Matrix-converter-based bidirectional buck–boost converter.

the size of the input ac filter and output dc filter is reduced which allows the fast response of this converter. It is capable of working as bidirectional buck and bidirectional boost converters and is an ideal solution for ac–dc conversion. It is derived from matrix converters normally used for ac–ac conversion for a wide frequency range at input as well as output. It can also be realized using GTOs for high power rating but with restricted switching frequency. Since it (GTO) does not need an additional series diode, it offers high efficiency.

G. Unidirectional Multilevel Converters [384]–[400]

Fig. 8 shows some of the basic circuits of these converters. The concept of multilevel is used to reduce the harmonics and switching losses in the converter through operating the switching devices at low switching frequency. Three-level three-phase converters can be implemented using either three devices [Fig. 8(a)] [398] or six devices [Fig. 8(b)] [400]. However, higher level converters such as a five level shown in Fig. 8(c) [400] require a higher number of devices but can avoid PWM switching losses while maintaining the same level of performance in terms of power quality at input ac mains and regulated dc output. These converters also offer boost operation for the output voltage with unidirectional power flow. It has lower voltage stresses on the devices and avoids PWM switching of them and, therefore, it is an ideal converter for high-voltage and high-power applications. These converters can be developed for a high number of levels to offer reduced THD and improved power factor of supply current at input ac mains and reduced ripple and regulated dc output voltage under varying load conditions.

H. Bidirectional Multilevel Converters [401]–[430]

Fig. 9 shows some circuit diagrams of bidirectional multilevel converters. These are used at high power ratings at high voltages with boost voltage for bidirectional power flow. These are further classified as clamped diode type [Fig. 9(a)] [400] and (b) [401] for three and five level), flying capacitor type [Fig. 9(c)] [404], and cascaded type multilevel converters. These converters are recommended for high-power and bidirectional power flow applications such as battery energy storage systems [401], four-quadrant variable-speed ac motor drives [411], [414], HVdc transmissions, flexible alternating current transmission systems (FACTS) [428], and static var compensation, to offer high efficiency and low THD of voltage and currents in the absence of PWM switching. For low- and medium-power applications, the IGBT is an ideal device,

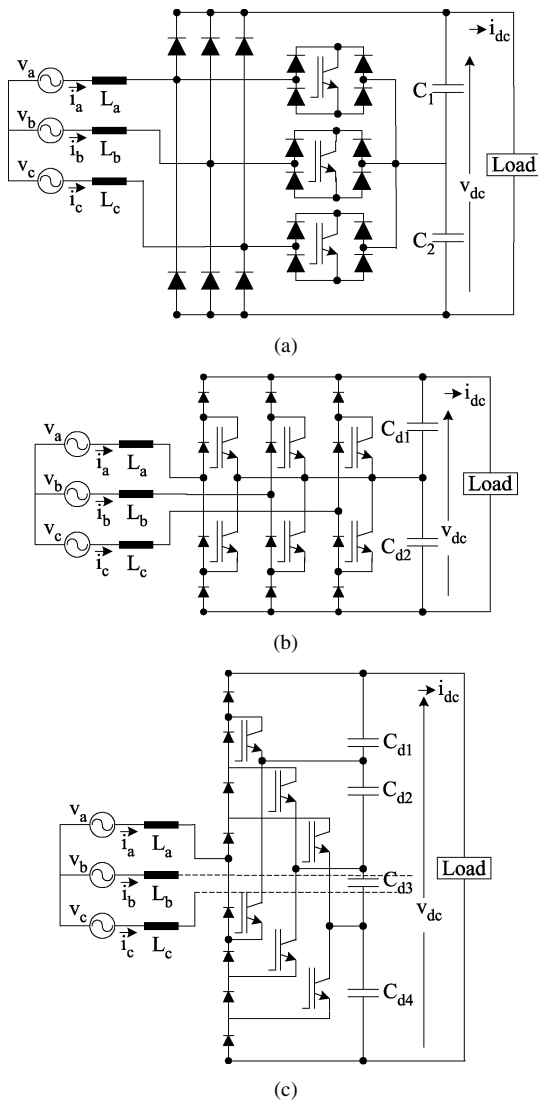


Fig. 8. (a) Three-switch unidirectional three-level converter. (b) Six-switch three-level unidirectional converter. (c) Unidirectional five-level converter.

however, for high-power applications, the GTO is invariably used. These converters provide a high level of power quality at input mains with reduced THD, high power factor and reduced EMI noise and boost, and ripple free, regulated dc output voltage insensitive to load and supply disturbances. They also avoid the use of transformers in some applications, which further enhances the efficiency of these converters.

I. Unidirectional Multipulse Converters [431]–[455]

Fig. 10 shows some of the circuits of these converters. Normally, diode bridges are used with a higher number of pulses for reducing harmonics in ac mains and reduced value of ripple voltage in the dc output. These are developed in 12-, 18-, 24-, 30-, 36-, 48-pulse, etc., converters, through input multipulse auto/isolation transformers and ripple current injection employing interphase reactors. It has been reported by many investigators that it is possible to reduce the THD of supply current below 2% even in 18-pulse converters [13], [433]. The rating, size, cost, and weight of different components of these converters are reduced using novel concepts in autotransformer

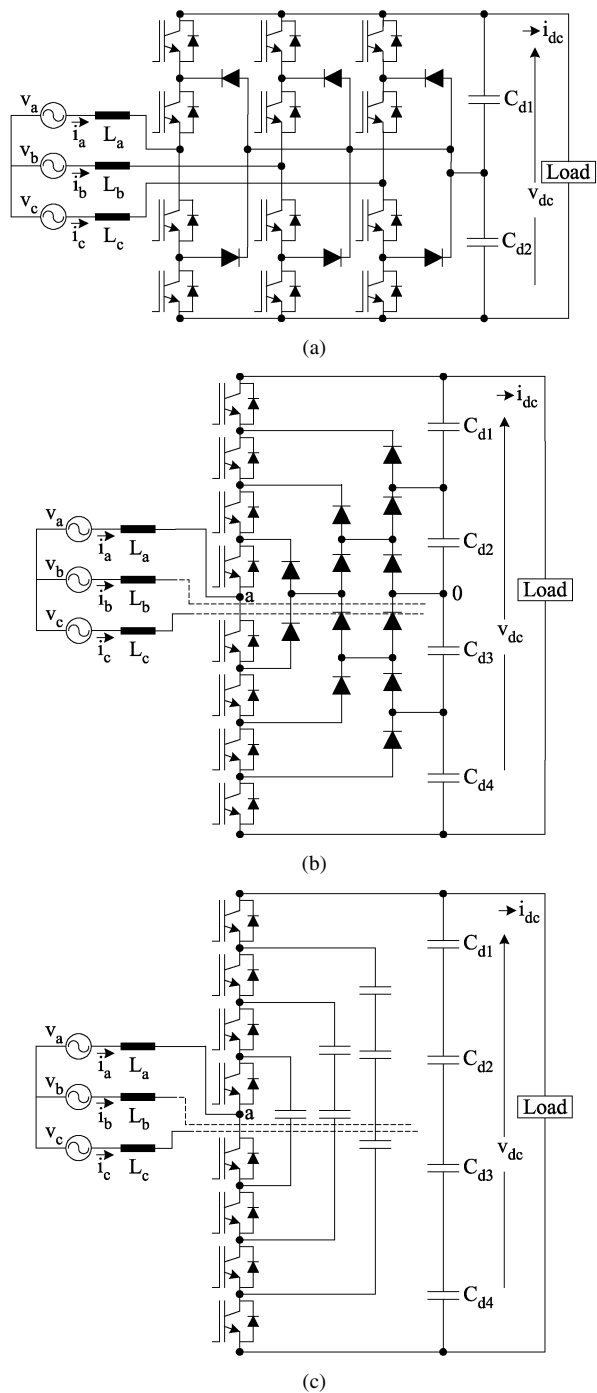


Fig. 9. (a) Three-level diode-clamped bidirectional converter. (b) Five-level diode-clamped bidirectional converter. (c) Five-level flying capacitor bidirectional converter.

configurations to achieve a higher number of phases from input three-phase AC mains through phase splitting at different angles, some of which are shown in Fig. 10(a) [13], (b) [13], and (c) [445] for 12-, 18-, and 24-pulse converters. The concepts of phase shift through input transformers and pulse multiplication through input tapped reactors and injection transformers at the dc link are at the heart of these converters. Normally, these converters employ only slow converter grade diodes, thus resulting in negligible switching losses and high efficiency, high power factor, low THD at input ac mains, and ripple-free dc output of high quality.

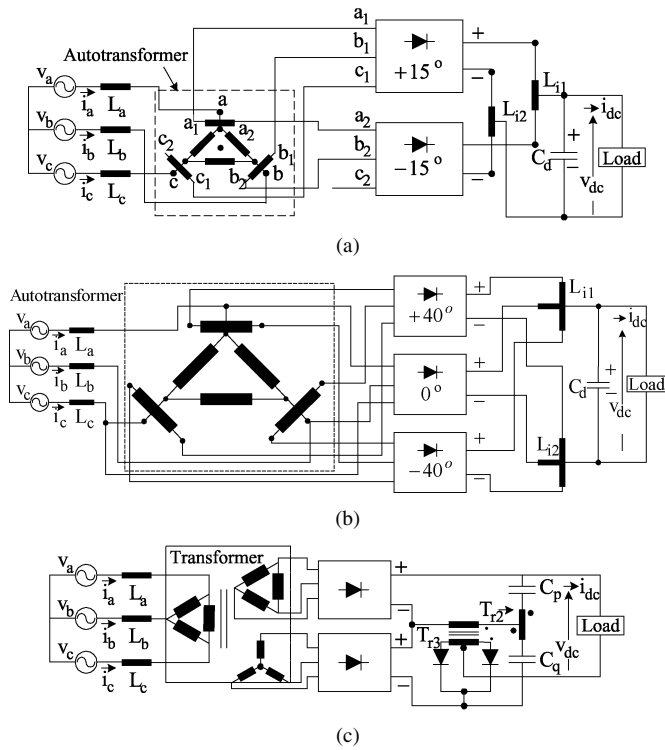


Fig. 10. (a) Unidirectional 12-pulse converter. (b) Unidirectional 18-pulse converter. (c) Unidirectional 24-pulse converter.

J. Bidirectional Multipulse Converters [456]–[477]

These converters normally use thyristors and harmonics reduction is made effective with pulse multiplication using magnetics [463]. Fig. 11 shows the two typical circuits of such converters [477]. The use of fully controlled thyristor bridge converters offers bidirectional power flow and adjustable output dc voltage. The use of a higher number of phases through an input multiple winding transformer and pulse multiplication using tapped reactor [463], and an injection transformer [456], reduces THD to input ac currents and ripples in the output dc voltage. These converters are used in high rating dc motor drives, HVdc transmission systems, and in some typical power supplies. Fig. 11(a) [477] shows a typical multipulse converter, which can be operated as a 6-, 12-, and 24-pulse converter. Similarly, the converter shown in Fig. 11(b) [458], [463] can be operated in 12-, 24-, and 48-pulse modes of operation. The cost and weight of input transformers can be reduced by using autotransformers in low- and medium-voltage applications.

IV. CONTROL OF IPQCS

The control strategy is the heart of IPQCs and is implemented in three parts. In the first part of the control algorithm, the essential variables used in control are sensed and scaled to feed to the processors for use in the control algorithm as the feedbacks. These signals are normally input ac mains voltages, supply currents, output dc voltage and, in some cases, additional voltages such as capacitor voltages and inductor currents are used in the intermediate stage of the converters. The ac voltage signals are sensed using potential transformers (PTs). Hall-effect voltage sensors, isolation amplifiers, and low-cost

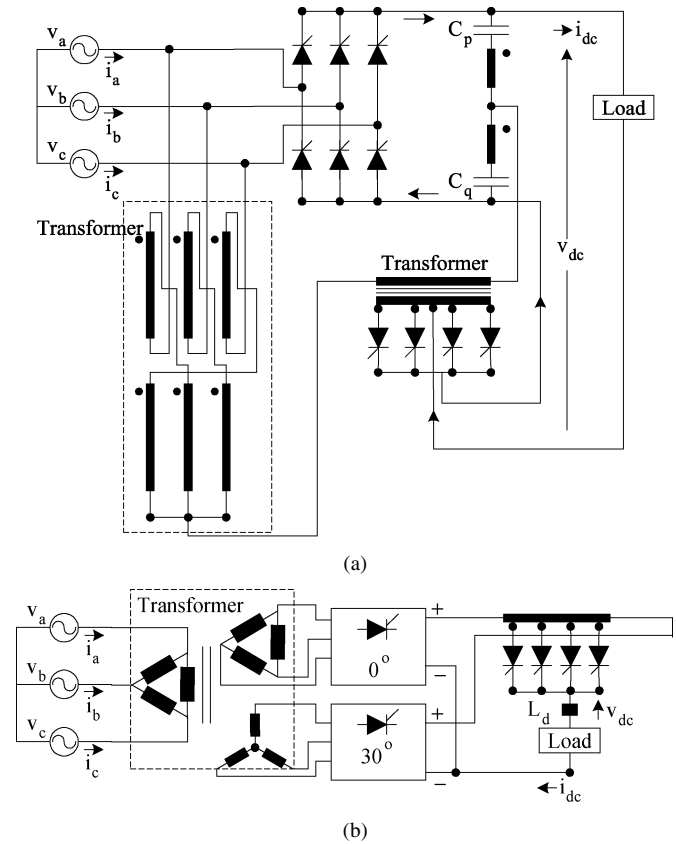


Fig. 11. (a) 24-pulse bidirectional midpoint reactor converter. (b) 48-pulse bidirectional converter.

optocouplers are used to sense dc voltages. These voltage signals are scaled and conditioned to the proper magnitude to feed to the processors via ADC channels or as the synchronizing signals for zero-crossing detection. The current signals are sensed using current transformers (CTs), Hall-effect current sensors, and low-cost shunt resistors or tapped isolated winding in the inductors to reduce the cost. These current signals are also conditioned and used as feedbacks at different stages of control either in the control algorithm or in the current control stage such as in PWM controllers or in both stages of control. These signals are sometimes filtered either through analog hardware circuits or through software in the processor to avoid noise problems in the control. These sensed voltage and current signals are also used sometimes to monitor, measure, protect, record, and display the various performance indexes such as THD, displacement factor, distortion factor, power factor, crest factor, individual harmonics, ripple factor, percentage ripples, sag and swell, surges and spikes, components stresses, etc. The cost of these sensing devices such as Half-effect sensors and other components used in sensing are being drastically reduced day by day because of mass manufacturing and competition among manufacturers. Moreover, some indirect sensing of these signals is also used through additional feedback nodes (terminal) in the IPM of MOSFETs and IGBTs to reduce the cost and to enhance the reliability of the converter.

The second stage of control is the control algorithm responsible for the transient and steady-state performance of the IPQCs. The control algorithm is implemented through analog

controllers or low-cost microcontrollers in low-power-rating converters. However, the DSPs and application-specific integrated circuits (ASICs) are used to control converters of high power ratings in sophisticated systems, depending upon the customer requirements. Normally, the dc output voltage of converters is the system output used as feedback in outer closed loop control. Various control approaches such as the PI control, proportional–integral–derivative (PID) control, sliding-mode control (SMC) [104], [138], [150], [175], [196], [218], [224], [318] also known as variable-structure control (VSC), fuzzy logic controllers (FLCs) [139], [161], [192], [202], [226], adaptive controllers, neural-network (NN)-based controllers [140], [149], [300], [302] are employed to provide fast dynamic response while maintaining the stability of the converter system over the wide operating range. The output of voltage controller is normally considered the amplitude of input ac mains current or indirect derived current such as inductor current and multiplied with unity template in phase with ac voltages to derive the reference desired unity power factor and sinusoidal supply currents.

The third stage of the control strategy of the IPQCs is to derive the gating signals for the solid-state devices of the converters. Reference supply currents along with sensed supply currents are used in the current controllers, which directly generate switching signals. A number of current controllers, namely, hysteresis, PWM current or PWM voltage control employing proportional, PI-, PID-, SMC-, FLC-, and NN-based controllers, are implemented either through hardware (analog and digital ICs) or through software in the same processors (DSPs or microcontrollers, which are used in the second stage) to derive gating signals. Nowadays, processors are available which are developed only for power electronics applications and have dedicated PWM controllers as a built-in feature to implement concurrently all three stages of control strategy for improving transient and steady-state performance of the IPQCs.

Moreover, in some control approaches, the second and third stage of control strategy of IPQCs are implemented in the integrated manner using some transformations such as “ $\alpha - \beta$,” “d-q” over the sensed voltage and current signals. The transformed voltage and/or current or derived power signals are used in the closed loop controllers to derive reference current or voltage signals for generating gating signals. The concurrent and integrated implementation of three stages of control algorithm provides cost effective, compact, fast response of the IPQCs.

V. COMPONENTS SELECTION FOR IPQCS

Selection of components of IPQCs is very important to achieve a high level of performance of ac–dc converters. The main and costly component of the IPQCs is the solid-state power devices. In low-power-rating converters, MOSFETs are normally used, resulting in reasonably high efficiency even at high switching frequency, which is responsible for reducing the size of magnetics. In the medium power rating of IPQCs, IGBTs are invariably used because of their good gating characteristics and capability of operating in a wide switching frequency range to make an optimum balance between magnetics, size of filter components, and switching losses. At high

power ratings, GTOs are normally used, with the advantages of self-commutating and reverse voltage-blocking capability. In multipulse converters, thyristors and diodes are still employed with the expected level of performance of IPQCs.

The concepts of power module, IPM, smart devices, etc., have given a real boost to IPQC technology because of circuit integration, compactness, cost reduction, reduced noise, and high efficiency. With several power devices in one module along with their gating and protection integration, it has become possible to get small-sized and lightweight IPQCs. In many cases, the complete control of IPQCs is also integrated in the same module along with the modifications to suit specific applications.

Other components of IPQCs are energy storage elements such as inductors, capacitors, and other devices used in filters, protection circuits, and resonating circuits. For example, a series inductor at the input of a VSI bridge working as a bidirectional boost converter is normally employed as the buffer element between ac mains voltage and PWM voltage generated by the converter to shape the input current in a desired manner. The value of this inductor is quite crucial in the performance of IPQCs. With the small value of this inductor the large switching ripples are injected into the supply current, and large value does not allow shaping the ac mains current in a desired fashion. Therefore, the optimum selection of this inductor is essential to achieve satisfactory performance of the IPQCs. Similarly, the value of the capacitor and inductor as the input filter in a buck converter is also quite important for proper response, stability, and optimum design of the IPQCs. Moreover, designs of inductors are also very important to avoid saturation and reducing losses under ac, dc, and mixed excitation. The value of the dc-bus capacitor in boost converters is quite crucial as it affects the response, cost, stability, size, and efficiency. A small value of dc-link capacitor results in a large ripple in steady state and a big dip and rise in dc-link voltage under transient conditions. A high value of it reduces the dc voltage ripple but increases cost, size, and weight.

Transformers operating at low frequency are used in multipulse converters in which transformer connection, weight, size, and rating are quite important. There are continuous attempts to reduce their size and cost through new configurations and with the use of tapped reactors at the dc link. However, high-frequency transformers are used in isolated topologies of IPQCs and their design is very important to reduce size, cost, and losses. The use of newer magnetic materials and operating frequency plays an important role to revolutionize the technology of IPQCs.

VI. COMPARATIVE FACTORS OF IPQCS AND OTHER OPTIONS OF POWER QUALITY IMPROVEMENT

The IPQCs classified in ten categories mentioned in the previous section do not clash with each other in the way of ac–dc conversion and have all together different features to suit a number of applications. Therefore, according to the requirement of application and/or second-stage conversion, a particular choice of IPQCs may be considered to provide the best suitable option. However, within the same category of IPQCs, there are many circuits which have relative merits

and demerits toward ideal characteristics. These additional configurations have improved performance but at higher cost. Therefore, the designer has to decide on a configuration of particular IPQCs on the basis of a tradeoff between performance and cost. Similar comparison exists for other IPQCs within these different configurations. In some cases, a choice can also be made among different IPQCs for specific applications. However, in such case, there are not many options for the designer to select and one can have a straightforward decision to opt for the right IPQC, which offers better performance at comparable cost. There are also some other options for power quality improvement in ac–dc conversion. For example, one can choose a series active filter or shunt active filter or hybrid filter in the input of the diode rectifier with a capacitive filter at the dc output to feed a number of dc loads [7]–[9]. Moreover, in IPQCs, also, one can choose a multipulse converter or three-phase unidirectional boost converter with one device, two devices, or multilevel configuration. It means one can have a number of options to select one of the best configurations of the converter for a particular application. For example, if a diode bridge rectifier is already working on site, then filters may be the right option in such a case. Moreover, one has to choose the best filter configuration among all possible options. However, if a designer is at the deciding design stage, then IPQCs may be the better option, which may provide improved performance in terms of output dc voltage regulation and high power factor and low THD of mains current. Similar situations may occur in the number of cases and the design engineer must be aware of all possible options and their relative features to select the best converter from an overall point of view.

VII. SELECTION OF IPQCS FOR SPECIFIC APPLICATIONS

Selection of IPQCs for a specific application is an important decision. The following are some of the factors responsible for selection of right converter for specific applications:

- required level of power quality in input (permitted PF, CF, THD);
- Type of output dc voltage (constant, variable, etc.);
- power flow (unidirectional and bidirectional);
- number of quadrants (one, two, or four);
- nature of output DC (isolated, nonisolated);
- requirement of output dc (buck, boost, and buck–boost);
- required level of power quality in output (voltage ripple, sag, and swell);
- type of dc load (linear, nonlinear, etc.);
- cost, size, and weight;
- efficiency;
- noise level (EMI, RFI, etc.);
- rating (kilowatt, megawatt, etc.);
- reliability;
- environment (ambient temperature, altitude, pollution level, humidity, types of cooling, etc.)

These are only some factors. There are some more considerations such as comparative features of other options of power quality improvement, types of device, magnetic components, protection, etc., in the selection of best IPQCs for a specific application.

VIII. LATEST TRENDS AND FURTHER DEVELOPMENTS IN IPQCS

IPQC technology has developed to a mature level and is employed in widespread applications in fraction of kilowatt to megawatt converter systems such as UPSs, ac–dc–ac links, BESSs, ASDs, etc. However, there are new developments in IPQCs for further improvements in their performance. The new trends are improved control algorithms and soft-switching techniques to reduce switching losses in IPQCs even at high switching frequency, to enhance the dynamic response, and to reduce the size of energy storage elements (filters at input and output, high-frequency transformers). The new developments toward single-stage conversion have resulted in increased efficiency, reduced size, high reliability, and compactness of IPQCs.

Sensor reduction has also revolutionized the IPQC technology to reduce their cost and enhance their reliability. Novel configurations in autotransformers for multipulse converters have resulted in their reduced size, cost, rating, weight, and losses. The new approaches in multilevel converters are offering high efficiency, reduced stress on devices, and a low level of high-frequency noise.

The further improvement in solid-state device technology in terms of low conduction losses, higher permissible switching frequency, ease in gating process, and new devices, especially low voltage drop and reduced switching losses, will give a real boost for IPQCs in low-voltage dc power applications. The multiple device integration into a single power module as a cell for direct use as a configuration of IPQCs will result in size reduction, increased efficiency, and low-cost option. The sensors, control, gating, and protection integration in the IPM will provide a new direction in the development of IPQCs. Dedicated processors and ASICs development for IPQCs are also expected in the near future to reduce their cost, provide ease in control, and result in compact and efficient ac–dc conversion. The invention of new configurations and reduction in conversion stages in IPQCs will help explore a number of newer applications.

IX. CONCLUSION

A comprehensive review of three-phase IPQCs has been carried out to explore a broad perspective on their different configurations to researchers, design and application engineers, and end users of ac–dc converters. The proposed classification of IPQCs in ten categories with further subclassification of various circuits is expected to provide an easy selection of an appropriate converter for a specific application. These IPQCs may be considered to be better alternatives for power quality improvement because of reduced size of the overall converter, higher efficiency, lower cost, and enhanced reliability compared to other means of power quality improvement. These converters provide improved power quality not only at input ac mains but also at dc output for better design of the overall equipment. Moreover, the use of these IPQCs results in any equipment behaving as a linear resistive load at the ac mains. The new developments in device technology, processors, magnetics, and control algorithms, will result in a real boost to these IPQCs in the near future.

REFERENCES

Standards, Texts, and Review Articles

- [1] *IEEE Recommended Practices and Requirements for Harmonics Control in Electric Power Systems*, IEEE Std. 519, 1992.
- [2] *Electromagnetic Compatibility (EMC)—Part 3: Limits—Section 2: Limits for Harmonic Current Emissions (Equipment Input Current < 16 A per Phase)*, IEC1000-3-2 Doc., 1995.
- [3] *Draft-Revision of Publication IEC 555-2: Harmonics, Equipment for Connection to the Public Low Voltage Supply System*, IEC SC 77A, 1990.
- [4] W. M. Grady, M. J. Samotyj, and A. H. Noyola, "Survey of active power line conditioning methodologies," *IEEE Trans. Power Delivery*, pp. 1536–1542, July 1990.
- [5] R. Ridley, "Three-phase power factor correction circuits—part 1," in *Proc. HFPC'94*, 1994, pp. 278–321.
- [6] D. Borojevic, "Analog vs. digital design three-phase power factor correction—Part 2," in *Proc. HFPC'94*, 1994, pp. 322–348.
- [7] H. Akagi, "New trends in active filters for power conditioning," *IEEE Trans. Ind. Applicat.*, vol. 32, pp. 1312–1322, Nov./Dec. 1996.
- [8] B. Singh, K. A.K. Al. Haddad, and A. Chandra, "A review of active filters for power quality improvement," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 960–971, Oct. 1999.
- [9] M. El-Habrouk, M. K. Darwish, and P. Mehta, "Active power filters: a review," *Proc. IEE—Elect. Power Applicat.*, vol. 147, pp. 493–413, Sept. 2000.
- [10] G. Seguier, *Power Electronic Converters AC/DC Conversion*. New York: McGraw-Hill, 1986.
- [11] G. T. Heydt, *Electric Power Quality*, 2nd ed. West Lafayette, IN: Stars in a Circle, 1994.
- [12] N. Mohan, T. Udeland, and W. Robbins, *Power Electronics: Converters, Applications and Design*, 2nd ed. New York: Wiley, 1995.
- [13] D. A. Paice, *Power Electronic Converter Harmonics—Multi-Pulse Methods for Clean Power*. New York: IEEE Press, 1996.
- [14] R. C. Dugan, M. F. McGranaghan, and H. W. Beaty, *Electric Power Systems Quality*. New York: McGraw-Hill, 1996.
- [15] J. Arrillaga and B. Smith, *AC-DC Power System Analysis*. Herts, U.K.: IEE Press, 1998.
- [16] G. J. Porter and J. A. V. Sciver, Eds., *Power Quality Solutions: Case Studies for Troubleshooters*. Lilburn, GA: Fairmount, 1999.
- [17] M. H. J. Bollen, *Understanding Power Quality Problems: Voltage Sags and Interruptions*, ser. Power Engineering. New York: IEEE Press, 2000.
- [18] D. Boroyevich and S. Hiti, "Three-phase PWM converter: Modeling and control design," presented at the IEEE APEC'96 (Seminar 9), San Jose, CA, Mar. 3–7, 1996.
- [19] P. Enjeti and I. Pitel, "Design of three-phase rectifier systems with clean power characteristics," presented at the IEEE PESC'99 (Tutorial), Charleston, SC, June 27 July 1, 1999.
- [20] J. W. Kolar and J. Sun, "Three-phase power factor correction technology," presented at the IEEE PESC'01 (Seminars 1 & 4), Vancouver, BC, Canada, June 17–22, 2001.
- [21] M. Rastogi, R. Naik, and N. Mohan, "A comparative evaluation of harmonic reduction techniques in three-phase utility interface of power electronic loads," *IEEE Trans. Ind. Applicat.*, vol. 30, pp. 1149–1155, Nov./Dec. 1994.
- [22] H. Mao, F. C. Y. Lee, and D. Boroyevich, "Review of high-performance three-phase power-factor correction circuits," *IEEE Trans. Ind. Electron.*, vol. 44, pp. 437–446, Aug. 1997.
- [23] G. A. Karvelis, S. N. Manias, and G. Kostakis, "A comparative evaluation of power converters used for current harmonics elimination," in *Proc. IEEE HQP'98*, 1998, pp. 227–232.
- [24] Y. Jang and M. Jovanovic, "A comparative study of single-switch three-phase high-power-factor rectifiers," *IEEE Trans. Ind. Applicat.*, vol. 34, pp. 1327–1334, Nov./Dec. 1998.
- [25] J. W. Kolar and H. Ertl, "Status of the techniques of three-phase rectifier systems with low effects on the mains," in *Proc. IEEE INTELEC'99*, 1999, Paper 14-1.
- [26] A. R. Prasad, P. D. Ziogas, and S. Manias, "An active power factor correction technique for three-phase diode rectifiers," *IEEE Trans. Power Electron.*, vol. 6, pp. 83–92, Jan. 1991.
- [27] R. Naik, M. Rastogi, and N. Mohan, "Third harmonic modulated power electronics interface with 3-phase utility to provide a regulated DC output and to minimize line-current harmonics," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1992, pp. 689–694.
- [28] D. S. L. Simonetti, J. Sebastian, and J. Uceda, "Single-switch three-phase power factor preregulator under variable switching frequency and discontinuous input current," in *Proc. IEEE PESC'93*, 1993, pp. 657–662.
- [29] R. Naik, M. Rastogi, N. Mohan, R. Nilssen, and C. P. Henze, "A magnetic device for current injection in a three-phase, sinusoidal-current utility interface," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1993, pp. 926–930.
- [30] J. W. Kolar, H. Ertl, and F. C. Zach, "A comprehensive design approach for a three-phase high frequency single-switch discontinuous-mode boost power factor corrector based on analytically derived normalized converter component ratings," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1993, pp. 931–938.
- [31] N. Mohan, M. Rastogi, and R. Naik, "Analysis of new power electronics interface with approximately sinusoidal 3-phase utility currents and a regulated DC output," *IEEE Trans. Power Delivery*, vol. 8, pp. 540–546, Apr. 1993.
- [32] R. J. Tu and C. L. Chen, "A new three-phase space-vector-modulated power factor corrector," in *Proc. IEEE APEC'94*, 1994, pp. 725–730.
- [33] S. Gataric, D. Borojevic, and F. C. Lee, "Soft-switched single-switch three-phase rectifier with power factor correction," in *Proc. IEEE APEC'94*, 1994, pp. 738–744.
- [34] G. Spiazzi and F. C. Lee, "Implementation of single-phase boost power factor correction circuits in three-phase applications," in *Proc. IEEE IECON'94*, 1994, pp. 250–255.
- [35] M. Tou, K. Al-Haddad, and V. Rajagopalan, "Unity power factor boost topology applied on three-phase rectifier," in *Proc. PCIM (USA)'94*, 1994, pp. 431–440.
- [36] J. W. Kolar, U. Drogenik, and F. C. Zach, "Space vector based analysis of the variation and control of the neutral point potential of hysteresis current controlled three-phase/switch/level PWM rectifier systems," in *Proc. IEEE PEDS'95*, 1995, pp. 22–33.
- [37] C. A. B. Munoz and I. Barbi, "A new high power factor three-phase diode rectifier," in *Proc. IEEE IECON'95*, 1995, pp. 451–456.
- [38] K. Chen, A. Elasser, and D. A. Torrey, "A soft switching active snubber optimized for IGBT's in single switch unity power factor three-phase diode rectifiers," *IEEE Trans. Power Electron.*, vol. 10, pp. 446–452, July 1995.
- [39] J. W. Kolar, H. Ertl, and F. C. Zach, "Space vector-based analytical analysis of the input current distortion of a three-phase discontinuous-mode boost rectifier system," *IEEE Trans. Power Electron.*, vol. 10, pp. 733–745, Nov. 1995.
- [40] M. Rastogi, N. Mohan, and C. P. Henze, "Three-phase sinusoidal current rectifier with zero-current switching," *IEEE Trans. Power Electron.*, vol. 10, pp. 753–759, Nov. 1995.
- [41] J. W. Kolar, H. Ertl, and F. C. Zach, "Design and experimental investigation of a three-phase high power density high efficiency unity power factor PWM (VIENNA) rectifier employing a novel integrated power semiconductor module," in *Proc. IEEE APEC'96*, 1996, pp. 514–523.
- [42] M. Tognoli and A. C. Rufer, "A DSP based control for a symmetrical three-phase two-switch PFC-power supply for variable output voltage," in *IEEE PESC'96*, 1996, pp. 1588–1594.
- [43] T. J. Omedi and R. Barlik, "Three-phase AC-DC unidirectional PWM rectifier topologies—selected properties and critical evaluation," in *IEEE ISIE'96*, 1996, pp. 784–789.
- [44] M. S. Dawande, V. R. Kanetkar, and G. K. Dubey, "Three-phase switch mode rectifier with hysteresis current control," *IEEE Trans. Power Electron.*, vol. 11, pp. 466–471, May 1996.
- [45] M. S. Dawande and G. K. Dubey, "Programmable input power factor correction methods for switch-mode rectifiers," *IEEE Trans. Power Electron.*, vol. 11, pp. 585–591, July 1996.
- [46] J. C. Salmon, "Operating a three-phase diode rectifier with a low-input current distortion using a series connected dual boost converter," *IEEE Trans. Power Electron.*, vol. 11, pp. 592–603, July 1996.
- [47] B. R. Lin, D. P. Wu, and T. F. Shiue, "Three-phase PFC with space vector and hysteresis control methods," in *Proc. IEEE PEDS'97*, 1997, pp. 48–52.
- [48] K. Hirachi, K. Nishimura, M. Kurokawa, and M. Nakaoka, "An improved circuit configurations of three-phase PFC converter," in *Proc. IEEE PEDS'97*, 1997, pp. 596–604.
- [49] K. Tanuguchi, T. Morizane, N. Kimura, H. Irie, and K. Hirachi, "High-performance soft-switched three-phase converter with sinusoidal input current and unity power factor," in *Proc. IEEE APEC'97*, 1997, pp. 672–678.
- [50] R. Zhang and F. C. Lee, "Optimum PWM pattern for a three-phase boost DCM PFC rectifier," in *Proc. IEEE APEC'97*, 1997, pp. 895–901.

Unidirectional Boost Converters

- [51] K. Schenk and S. Cuk, "A simple three-phase power factor corrector with improved harmonic distortion," in *Proc. IEEE PESC'97*, 1997, pp. 399–405.
- [52] O. Garcia, J. A. Cobos, R. Prieto, and J. Uceda, "Single switch AC/DC power factor correction converter valid for both three-phase and single-phase applications," in *Proc. EPE'97*, 1997, pp. 1.188–1.193.
- [53] J. C. L. Bunetel, B. Dakyo, L. Protin, and W. Koczara, "Analytical approach of switching behavior and modeling of a single-phase parallel structure of boost type power factor corrector," in *Proc. EPE'97*, 1997, pp. 2.901–2.905.
- [54] C. H. Treviso, V. J. Farias, J. B. Vieira Jr., and L. C. D. Freitas, "A three-phase PWM boost rectifier with high power factor operation and an acceptable current THD using only three switches," in *Proc. EPE'97*, 1997, pp. 2.934–2.939.
- [55] Y. Nishida and M. Nakaoa, "Simplified predictive instantaneous current control for single-phase and three-phase voltage fed PFC converters," *Proc. IEE—Elect. Power Applicat.*, vol. 144, pp. 46–52, Jan. 1997.
- [56] E. L. M. Mehl and I. Barbi, "An improved high-power factor and low-cost three-phase rectifier," *IEEE Trans. Ind. Applicat.*, vol. 33, pp. 485–492, Mar./Apr. 1997.
- [57] M. Tou, K. Al-Haddad, G. Olivier, and V. Rajagopalan, "Analysis and design of single-controlled switch three-phase rectifier with unity power factor and sinusoidal input current," *IEEE Trans. Power Electron.*, vol. 12, pp. 608–617, July 1997.
- [58] J. W. Kolar and F. C. Zach, "A novel three-phase utility interface minimizing line current harmonics of high-power telecommunications rectifier module," *IEEE Trans. Ind. Electron.*, vol. 44, pp. 456–467, Aug. 1997.
- [59] F. Daniel, R. Chaffai, K. Al-Haddad, and R. Parimelalagan, "A new modulation technique for reducing the input current harmonics of a three-phase diode rectifier with capacitive load," *IEEE Trans. Ind. Applicat.*, vol. 33, pp. 1185–1193, Sept./Oct. 1997.
- [60] J. C. Salmon, E. Nowicki, W. Xu, and D. Koval, "Low distortion 3-phase rectifiers utilizing harmonic correction circuit topologies with both IGBT and thyristor switches," in *Proc. IEEE APEC'98*, vol. 2, 1998, pp. 1100–1106.
- [61] Y. Jang and M. M. Jovanovic, "A comparative study of single-switch, three-phase high-power-factor rectifiers," in *Proc. IEEE APEC'98*, 1998, pp. 1093–1099.
- [62] W. C. P. D. A. Filho and I. Barbi, "A single stage high power factor 3 phase 60 V/100 A power supply using a line-side interphase transformer and an isolated push-pull converter," in *Proc. IEEE APEC'98*, 1998, pp. 1114–1119.
- [63] C. Qiao and K. M. Smedley, "A general three-phase PFC controller part I. For rectifiers with a parallel-connected dual boost topology," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1998, pp. 2504–2511.
- [64] ———, "A general three-phase PFC controller part II. For rectifiers with a series-connected dual boost topology," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1998, pp. 2512–2519.
- [65] D. Carlton, W. G. Dunford, and M. Edmunds, "Harmonic reduction in three-phase 3-switches boost-delta power factor correction circuit operating in discontinuous mode," in *Proc. IEEE INTELEC'98*, 1998, pp. 483–490.
- [66] R. J. Tu and C. L. Chen, "A new space-vector-modulated control for a unidirectional three-phase switch-mode rectifier," *IEEE Trans. Ind. Electron.*, vol. 45, pp. 256–262, Apr. 1998.
- [67] E. H. Ismail, C. M. Oliveira, and R. W. Erickson, "A low-distortion three-phase multiresonant boost rectifier with zero-current switching," *IEEE Trans. Power Electron.*, vol. 13, pp. 718–726, July 1998.
- [68] Y. Jang and M. M. Jovanovic, "A novel robust harmonic injection method for single-switch three-phase discontinuous-conduction-mode boost rectifiers," *IEEE Trans. Power Electron.*, vol. 13, pp. 824–834, Sept. 1998.
- [69] R. Blundell, L. Kupka, and S. Spiteri, "AC-DC converter with unity power factor and harmonic content of line current: design consideration," *Proc. IEE—Elect. Power Applicat.*, vol. 145, no. 6, pp. 553–558, Nov. 1998.
- [70] P. Pejovic and Z. Janda, "Optimal current programming in three-phase high-power-factor rectifier based on two boost converters," *IEEE Trans. Power Electron.*, vol. 13, pp. 1152–1163, Nov. 1998.
- [71] D. Carlton, W. G. Dunford, and M. Edmunds, "Delta-delta three-phase boost power factor correction circuit operating in discontinuous conduction mode," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1999, pp. 2497–2502.
- [72] J. W. Kolar, U. Drofenik, and F. C. Zach, "VIENNA rectifier II—A novel single-stage high frequency isolated three-phase PWM rectifier system," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 674–691, Aug. 1999.
- [73] D. F. Weng and S. Yuvarajan, "Resonant-boost-input three-phase power factor corrector," *IEEE Trans. Power Electron.*, vol. 14, pp. 1149–1155, Nov. 1999.
- [74] P. Ide, N. Froehleke, H. Grotstollen, W. Korb, and B. Margaritis, "Analysis of three-phase/three level rectifiers at low load and discontinuous conduction mode," in *Proc. IEEE APEC'00*, 2000, pp. 201–207.
- [75] B. N. Singh, P. Jain, and G. Joos, "Three-phase AC/DC regulated power supplies: a comparative evaluation of different topologies," in *Proc. IEEE APEC'00*, 2000, pp. 513–518.
- [76] R. Ayyanar, N. Mohan, and J. Sun, "Single-stage three-phase power-factor-correction circuit using three isolated single-phase SEPIC converters operating in CCM," in *Proc. IEEE PESC'00*, 2000, pp. 353–358.
- [77] J. C. Crebier, P. Barbosa, F. Canales, F. C. Lee, and J. P. Ferrieux, "Frequency domain analysis and evaluation of different mode input current for three-phase DCM boost rectifiers with different control strategies," in *Proc. IEEE PESC'00*, 2000, pp. 482–487.
- [78] J. W. Kolar, F. Stogerer, J. Minibock, and H. Ertl, "A new concept for reconstruction of the input phase currents of a three-phase/switch/level PWM (VIENNA) rectifier based on neutral point current measurement," in *Proc. IEEE PESC'00*, 2000, pp. 139–146.
- [79] H. Kanaan, H. F. Blanchette, K. A.K. Al. Haddad, R. Chaffai, and L. Duguay, "Modeling and analysis of a three-phase unity power factor current injection rectifier using one loop control strategy," in *Proc. IEEE INTELEC'00*, 2000, pp. 518–525.
- [80] B. N. Singh, G. Joos, and P. Jain, "A new topology of 3-phase PWM AC/DC interleaved converter for telecommunication supply system," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 2000, pp. 2290–2296.
- [81] M. V. Aware, S. G. Tarnekar, and A. G. Kothari, "Unity power factor and efficiency control of a voltage source inverter-fed variable-speed induction motor drive," *Proc. IEE—Elect. Power Applicat.*, vol. 147, pp. 422–430, Sept. 2000.
- [82] K. Nishimura, K. Atsumi, K. Tachibana, K. Hirachi, S. Moisseev, and M. Nakaoka, "Practical performance evaluations on an improved circuit topology of active three-phase PFC power converter," in *Proc. IEEE APEC'01*, 2001, pp. 1308–1314.
- [83] J. Hahn, P. N. Enjeti, and I. J. Pitel, "A new three-phase power factor correction (PFC) scheme using two single-phase PFC modules," in *Proc. IEEE APEC'01*, 2001, pp. 813–819.
- [84] J. Minibock, F. Stogerer, and J. W. Kolar, "A novel concept for mains voltage proportional input current shaping of a VIENNA rectifier eliminating controller multipliers, Part I: Basic theoretical considerations and experimental verification," in *Proc. IEEE APEC'01*, 2001, pp. 582–586.
- [85] H. Kanaan, K. A.K. Al. Haddad, R. Chaffai, R. Duguay, and F. Fnaiech, "A small-signal model of a three-phase current-injection-based rectifier," in *Proc. IEEE PESC'01*, 2001, pp. 688–694.
- [86] F. Stogerer, J. Minibock, and J. W. Kolar, "Implementation of a novel control concept for reliable operation of a VIENNA rectifier under heavily unbalanced mains voltage conditions," in *Proc. IEEE PESC'01*, 2001, pp. 1333–1338.

Bidirectional Boost Converters

- [87] E. Wernekinck, A. Kawamura, and R. Hoft, "A high frequency ac/dc converter with unity power factor and minimum harmonic distortion," in *Proc. IEEE PESC'87*, 1987, pp. 264–270.
- [88] N. Nishimoto, J. W. Dixon, A. B. Kulkarni, and B.-T. Ooi, "An integrated controlled-current PWM rectifier chopper link for sliding mode position control," *IEEE Trans. Ind. Applicat.*, vol. 23, pp. 894–900, Sept./Oct. 1987.
- [89] H. Kohlmeier, O. Niermeyer, and D. F. Schröder, "Highly dynamic four-quadrant ac motor drive with improved power and on-line optimized pulse pattern with PROMC," *IEEE Trans. Ind. Applicat.*, vol. 23, pp. 1001–1009, Sept./Oct. 1987.
- [90] J. W. Dixon, A. B. Kulkarni, M. Nishimoto, and B. T. Ooi, "Characteristics of a controlled-current PWM rectifier-inverter link," *IEEE Trans. Ind. Applicat.*, vol. 23, pp. 1022–1028, Nov./Dec. 1987.
- [91] J. W. Dixon and B. T. Ooi, "Dynamically stabilized indirect current controlled SPWM boost type 3-phase rectifier," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1988, pp. 700–705.
- [92] B. T. Ooi, J. W. Dixon, A. B. Kulkarni, and M. Nishimoto, "An integrated ac drive system using a controlled current PWM rectifier/inverter link," *IEEE Trans. Power Electron.*, vol. 3, pp. 64–71, Jan. 1988.
- [93] A. W. Green, J. T. Boys, and G. F. Gates, "3-phase voltage source reversible rectifier," *Proc. Inst. Elect. Eng.*, vol. 135, pp. 362–370, Nov. 1988.
- [94] J. W. Kolar, H. Ertl, and F. C. Zach, "Calculation of the passive and active component stress of the three phase PWM converter systems with high pulse rate," in *Proc. EPE'89*, 1989, pp. 1303–1311.

- [95] A. W. Green and J. T. Boys, "Hysteresis current-forced three-phase voltage source reversible rectifier," *Proc. Inst. Elect. Eng.*, vol. 136, pp. 113–120, May 1989.
- [96] J. W. Dixon, "Feedback control strategies for boost type PWM rectifiers," in *Proc. IEEE Colloq. South America '90*, 1990, pp. 193–198.
- [97] B. T. Ooi and X. Wang, "Voltage angle lock loop control of the boost type PWM converter for HVDC application," *IEEE Trans. Power Electron.*, vol. 5, pp. 229–235, Apr. 1990.
- [98] R. Wu, S. B. Dewan, and G. R. Slemon, "A PWM AC-DC-AC converter with fixed switching frequency," *IEEE Trans. Ind. Applicat.*, vol. 26, pp. 880–885, Sept./Oct. 1990.
- [99] Y. Itoh and S. Kawauchi, "Easy digital control of three-phase PWM converter," in *Proc. INTELEC '91*, 1991, pp. 727–734.
- [100] T. Ohnishi, "The-phase PWM converter/inverter by means of instantaneous active and reactive power control," in *Proc. IEEE IECON '91*, 1991, pp. 819–824.
- [101] T. Takeshita and N. Matsui, "DSP-based current control of three-phase AC/DC PWM converter with model reference adaptive identifier," in *Proc. IEEE PESC '92*, 1992, pp. 496–502.
- [102] R. Marschalko and M. Weinhold, "Optimal control and appropriate pulse width modulation for a three-phase voltage dc-link PWM converter," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1992, pp. 1042–1049.
- [103] K. Miyagawa, M. Nakaoka, Y. Ogino, Y. Murakami, and K. Hayashi, "Space-vector controlled soft-switching three-phase PDM ac/dc converter with unity power factor and sinusoidal line current," in *Proc. IEEE IECON '92*, 1992, pp. 209–216.
- [104] N. Sabanovic, A. Sabanovic, and K. Ohnishi, "Sliding mode control of three-phase switching power converters," in *Proc. IEEE IECON '92*, 1992, pp. 319–324.
- [105] L. Moran, P. D. Ziogas, and G. Joos, "Design aspects of synchronous PWM rectifier-inverter systems under unbalanced input voltage conditions," *IEEE Trans. Ind. Applicat.*, vol. 26, pp. 1286–1293, Nov./Dec. 1992.
- [106] T. Sukegawa, K. Kamiyama, J. Takahashi, T. Ikimi, and M. Matsutake, "A multiple GTO line-side converter for unity power factor and reduced harmonics," *IEEE Trans. Ind. Applicat.*, vol. 26, pp. 1302–1308, Nov./Dec. 1992.
- [107] F. Blaabjerg and J. K. Pedersen, "An integrated high power factor three-phase AC-DC-AC converter for AC machines implemented in one micro-controller," in *Proc. IEEE PESC '93*, 1993, pp. 285–292.
- [108] J. W. Choi and S. K. Sul, "Resonant link bi-directional power converter without electrolytic capacitor," in *Proc. IEEE PESC '93*, 1993, pp. 293–299.
- [109] J. S. Kim and S. K. Sul, "New control scheme for AC-DC-AC converter without dc link electrolytic capacitor," in *Proc. IEEE PESC '93*, 1993, pp. 300–306.
- [110] N. Š-Behililovic, T. Ninomiya, A. Šabanovic, and B. Perunicic, "Control of three-phase switching converters: A sliding mode approach," in *Proc. IEEE PESC '93*, 1993, pp. 630–635.
- [111] P. Rioual and H. Poulquien, "Regulation of a PWM rectifier in the unbalanced network state," in *Proc. IEEE PESC '93*, 1993, pp. 641–647.
- [112] N. Nakaoka, H. Yonemori, and K. Yurugi, "Zero-voltage soft-switched PDM three phase ac-dc active power converter operating at unity power factor and sinewave line current," in *Proc. IEEE PESC '93*, 1993, pp. 787–794.
- [113] V. Vlatković, D. Borojević, and F. C. Lee, "A new zero-voltage transition, three-phase PWM rectifier/inverter circuit," in *Proc. IEEE PESC '93*, 1993, pp. 868–873.
- [114] T. Singh and S. B. Dewan, "Modeling and control of a high power pulse width modulated synchronous rectifier," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1993, pp. 957–964.
- [115] J. M. Retif, B. Allard, X. Jorda, and A. Perez, "Use of ASIC's in PWM techniques for power converters," in *Proc. IEEE IECON '93*, 1993, pp. 683–688.
- [116] P. Verdelho and G. D. Marques, "Decoupled model of the PWM voltage converter connected to the ac mains," in *Proc. IEEE IECON '93*, 1993, pp. 1021–1026.
- [117] T. G. Habetler, "A space vector-based rectifier regulator for AC/DC/AC converters," *IEEE Trans. Power Electron.*, vol. 8, pp. 30–36, Jan. 1993.
- [118] J. W. Kolar, H. Ertl, and F. C. Zach, "Quasidual modulation of three-phase PWM converters," *IEEE Trans. Ind. Applicat.*, vol. 29, pp. 313–319, Mar./Apr. 1993.
- [119] C.-T. Pan and T.-C. Chen, "Modeling and analysis of a three phase PWM AC-DC converter without current sensor," *Proc. Inst. Elect. Eng.*, pt. B, vol. 140, pp. 201–208, May 1993.
- [120] Y. Ito, Y. Kanno, and S. Kawauchi, "Source voltage sensor-less digital control using observer for PWM converter," in *Proc. IEEE PESC '94*, 1994, pp. 160–165.
- [121] P. Roual, H. Poulquien, and J. P. Louis, "Non linear control of PWM rectifiers by state feedback linearization and exact PWM control," in *Proc. IEEE PESC '94*, 1994, pp. 1095–1102.
- [122] Y. Jiang, H. Mao, F. C. Lee, and D. Borojevic, "Simple high performance three-phase boost rectifiers," in *Proc. IEEE PESC '94*, 1994, pp. 1158–1163.
- [123] A. Draou, Y. Sato, and T. Kataoka, "A fast current control method for voltage type PWM ac-dc converter system," in *Proc. IEEE IECON '94*, 1994, pp. 366–371.
- [124] P. Tenti, A. Zuccato, L. Rossetto, and M. Bortolotto, "Optimum digital control of PWM rectifiers," in *Proc. IEEE IECON '94*, 1994, pp. 382–387.
- [125] L. Chen, F. Blaabjerg, and P. S. Frederiksen, "An improved predictive control for three-phase PWM ac/dc converter with low sampling frequency," in *Proc. IEEE IECON '94*, 1994, pp. 399–404.
- [126] B.-D. Min and B.-H. Kwon, "A unity power factor control for fully software-controlled three-phase PWM rectifier with voltage link," in *Proc. IEEE IECON '94*, 1994, pp. 555–560.
- [127] D. R. Veas, J. W. Dixon, and B.-T. Ooi, "A novel load current control method for a leading power factor voltage source PWM rectifier," *IEEE Trans. Power Electron.*, vol. 9, pp. 153–159, Mar. 1994.
- [128] Y. Guo, X. Wang, H. C. Lee, and B.-T. Ooi, "Pole-placement control of voltage-regulated PWM rectifiers through real-time multiprocessing," *IEEE Trans. Ind. Electron.*, vol. 41, pp. 224–230, Apr. 1994.
- [129] V. Vlatko and D. Borojevic, "Digital-signal-processor-based control of three-phase space vector modulated converters," *IEEE Trans. Ind. Electron.*, vol. 41, pp. 326–332, June 1994.
- [130] C. T. Rim, N. S. Choi, G. C. Cho, and G. H. Cho, "A complete dc and ac analysis of three-phase controlled current PWM rectifier using circuit d-q transform," *IEEE Trans. Power Electron.*, vol. 9, pp. 390–396, July 1994.
- [131] A. Draou, Y. Sato, and T. Kataoka, "New approach to current control of ac-to-dc voltage-type converters," *Proc. IEE—Elect. Power Applicat.*, vol. 141, pp. 275–283, Nov. 1994.
- [132] A. M. Hava, T. A. Lipo, and W. L. Erdman, "Utility interface issues for line connected PWM voltage source converters: A comparative study," in *Proc. IEEE APEC '95*, 1995, pp. 125–132.
- [133] L. Chen and F. Blaabjerg, "A three-phase predictive PWM Ac/DC converter with phase compensation and space vector control," in *Proc. IEEE APEC '95*, 1995, pp. 863–869.
- [134] S. Hiti, D. Borojevic, R. Ambatipudi, R. Zhang, and Y. Jiang, "Average current control of three-phase PWM boost rectifier," in *Proc. IEEE PESC '95*, 1995, pp. 131–137.
- [135] C.-T. Pan and M.-C. Jiang, "Control and implementation of a three-phase voltage-doubler reversible ac to dc converter," in *Proc. IEEE PESC '95*, 1995, pp. 437–443.
- [136] A. Veltman and J. L. Duarte, "Fish method based on-line optimal control for PWM rectifiers," in *Proc. IEEE PESC '95*, 1995, pp. 549–555.
- [137] M. B. Lindgren, "Feed forward—time efficient control of a voltage source converter connected to the grid by lowpass filter," in *Proc. IEEE PESC '95*, 1995, pp. 1028–1032.
- [138] P. Marino and F. Vasca, "Sliding mode control for three phase rectifiers," in *Proc. IEEE PESC '95*, 1995, pp. 1033–1039.
- [139] B. Singh, C. L. P. C. L. Putta Swamy, B. P. Singh, A. Chandra, and K. Al-Haddad, "Performance analysis of fuzzy logic controlled permanent magnet synchronous motor drive," in *Proc. IEEE IECON '95*, 1995, pp. 399–405.
- [140] H. Pinheiro, G. Joós, and K. Khorasani, "Neural network-based controllers for voltage source PWM front end rectifiers," in *Proc. IEEE IECON '95*, 1995, pp. 488–493.
- [141] S. Bhowmik, R. Spée, G. C. Alexander, and J. H. R. Enslin, "New simplified control algorithm for synchronous rectifiers," in *Proc. IEEE IECON '95*, 1995, pp. 494–499.
- [142] B. Singh, B. N. Singh, B. P. Singh, A. Chandra, and K. Al-Haddad, "Unity power factor converter-inverter fed vector controlled cage motor drive without mechanical speed sensor," in *Proc. IEEE IECON '95*, 1995, pp. 609–614.
- [143] Y.-K. Lo and C.-L. Chen, "Three-phase four wire voltage controlled ac line conditioners with unity input power factor and minimized output voltage harmonics," *Proc. IEE—Elect. Power Applicat.*, vol. 142, pp. 43–49, Jan. 1995.
- [144] N. R. Zargari and G. Joós, "Performance investigation of a current-controlled voltage-regulated PWM rectifier in rotating and stationary frames," *IEEE Trans. Ind. Electron.*, vol. 42, pp. 396–401, Aug. 1995.

- [145] M. E. Fraser, C. D. Manning, and B. M. Wells, "Transformerless four-wire PWM rectifier and its application in AC-DC-AC converters," *Proc. IEE—Elect. Power Applicat.*, vol. 142, pp. 410–416, Nov. 1995.
- [146] A. Draou, Y. Sato, and T. Kataoka, "A new state feedback based transient control of PWM AC to DC voltage type converters," *IEEE Trans. Power Electron.*, vol. 10, pp. 716–724, Nov. 1995.
- [147] L. J. Borle and C. V. Nayar, "Zero average current error controlled power flow for AC-DC power converters," *IEEE Trans. Power Electron.*, vol. 10, pp. 725–732, Nov. 1995.
- [148] T. Noguchi, H. Tomiki, S. Kondo, and I. Takahashi, "Direct power control of PWM converter without power source voltage sensors," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1996, pp. 941–946.
- [149] G. Joós, H. Pinheiro, and K. Khorasani, "DSP implementation of neural network-based controller for voltage PWM rectifier neural," in *Proc. IEEE TENCON'96*, 1996, pp. 883–888.
- [150] D. M. Vilathgamuwa, S. R. Wall, and R. D. Jackson, "Variable structure control of voltage sourced reversible rectifier," *Proc. IEE—Elect. Power Applicat.*, vol. 143, pp. 18–24, Jan. 1996.
- [151] G. T. Kim and T. A. Lipo, "VSI-PWM rectifier/inverter system with a reduced switch count," *IEEE Trans. Ind. Applicat.*, vol. 32, pp. 1331–1337, Nov./Dec. 1996.
- [152] S. Mazumder, "DSP based implementation of a PWM AC/DC/AC converter using space vector modulation with primary emphasis on the analysis of the practical problems involved," in *Proc. IEEE APEC'97*, 1997, pp. 306–312.
- [153] J. W. Lee and B. H. Kang, "Digital current controller with delay compensator for PWM converters," in *Proc. IEEE EMDC'97*, 1997, pp. TC2-6.1–TC2-6.3.
- [154] T. Ohnishi and K. Fujii, "Line voltage sensor-less three phase PWM converter by tracking control of operating frequency," in *Proc. IEEE PCC-Nagaoka'97*, 1997, pp. 247–252.
- [155] K. T. Park, J. K. Kang, and S. K. Sul, "Analysis and design of three-phase boost PWM converter against power disturbances," in *Proc. IEEE PCC-Nagaoka'97*, 1997, pp. 773–776.
- [156] A. Draou, "Stability analysis and performance evaluation of AC side current control system of voltage type converters," in *Proc. IEEE PCC-Nagaoka'97*, 1997, pp. 865–870.
- [157] J.-K. Kang and S.-K. Sul, "Control of unbalanced voltage PWM converter using instantaneous ripple power feedback," in *Proc. IEEE PESC'97*, 1997, pp. 503–508.
- [158] J.-W. Lee, "An intelligent current controller using delay compensation for PWM converters," in *Proc. EPE'97*, vol. 1, 1997, pp. 342–347.
- [159] D.-C. Lee, G.-M. Lee, and D.-H. Kim, "Multivariable state feedback control for three-phase power conversion systems," in *Proc. EPE'97*, vol. 1, 1997, pp. 348–353.
- [160] P. Verdelho and G. D. Marques, "A unity power factor PWM voltage rectifier under nonsinusoidal and unbalanced conditions," in *Proc. EPE'97*, vol. 2, 1997, pp. 250–255.
- [161] J. Rodríguez, J. Hernández, M. Salgado, and F. Liebe, "Control of a three-phase PWM front end rectifier using fuzzy logic," in *Proc. EPE'97*, vol. 2, 1997, pp. 438–443.
- [162] C. Colliez, A. Tounzi, and F. Priou, "Vector control of an autonomous induction generator connected to a PWM rectifier," in *Proc. EPE'97*, vol. 2, 1997, pp. 711–716.
- [163] T. Ohnuki, O. Miyashita, P. Ph. Lataire, and G. Maggetto, "A three-phase PWM rectifier without voltage sensors," in *Proc. EPE'97*, vol. 2, 1997, pp. 881–886.
- [164] F. R. Walsh, J. F. Moynihan, P. J. Roche, M. G. Egan, and J. M. D. Murphy, "Analysis and influence of modulation scheme on the sizing of the input filter in a PWM rectifier system," in *Proc. EPE'97*, vol. 2, 1997, pp. 929–933.
- [165] B.-R. Lin, D.-P. Wu, and T.-F. Shiu, "Three-phase power reversible converter with simple control algorithm," in *Proc. EPE'97*, vol. 3, 1997, pp. 189–194.
- [166] V. Vlaouh and J. Škramlík, "Analysis of direct self control in voltage-type PWM rectifier," in *Proc. EPE'97*, vol. 3, 1997, pp. 195–199.
- [167] A. Sikorski, "Optimization of the ac/dc/ac converter current control loop," in *Proc. EPE'97*, vol. 3, 1997, pp. 200–205.
- [168] J. Sakly, S. Toumia, S. Hassine, N. Bouguila, C. Rombaut, and J. P. Cambronne, "New control strategies for high power, current source, PWM rectifiers using instantaneous spatial vector technique," in *Proc. EPE'97*, vol. 3, 1997, pp. 212–215.
- [169] J. Svensson, "Inclusion of dead-time and parameter variations in VSC modeling for predicting responses of grid voltage harmonics," in *Proc. EPE'97*, vol. 3, 1997, pp. 216–221.
- [170] G. D. Marques and J. F. Silva, "Direct voltage control of a PWM ac/dc voltage converter," in *Proc. EPE'97*, vol. 3, 1997, pp. 222–227.
- [171] O. Apeldoorn, T. Skullerud, R. D. Doncker, and T. Undeland, "Minimizing the line interference of high-power low-frequency converters," in *Proc. EPE'97*, vol. 3, 1997, pp. 250–255.
- [172] A. Draou, "Design criteria and performance analysis of ac side current control system of ac-to-dc converters," in *Proc. EPE'97*, vol. 3, 1997, pp. 313–318.
- [173] L. Sack, "DC link currents in bi-directional power converters with coordinated pulse patterns," in *Proc. EPE'97*, vol. 4, 1997, pp. 239–244.
- [174] J. Ollila, "The space vector control of the PWM-rectifier using U/F references," in *Proc. EPE'97*, vol. 4, 1997, pp. 245–249.
- [175] C. W. Lu and A. C. Renfrew, "Variable structure current control in current controlled voltage sourced rectifier," in *Proc. EPE'97*, vol. 4, 1997, pp. 854–858.
- [176] S. Buso, L. Malesani, P. Mattavelli, and R. Veronese, "Design and fully digital control of parallel active filters for thyristor rectifiers," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1997, pp. 1360–1367.
- [177] I. G. Park and S. I. Kim, "A new thyristor phase-controlled voltage source converter with bi-directional power flow capability," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1997, pp. 1368–1375.
- [178] Y. Sato, T. Ishizuka, K. Nezu, and T. Kataoka, "A new control strategy for voltage type PWM rectifiers to realize zero steady-state control error in input current," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1997, pp. 1496–1503.
- [179] T. Nakajima, "Development and testing of prototype models of a 300 MW GTO converter for power system interconnection," in *Proc. IEEE IECON'97*, 1997, pp. 423–429.
- [180] D.-K. Min, S.-C. Ahn, and D.-S. Hyun, "Control of a PWM converter in different input voltage conditions," in *Proc. IEEE IECON'97*, 1997, pp. 882–887.
- [181] V. Blasko and V. Kaura, "A new mathematical model and control of a three-phase ac-dc voltage source converter," *IEEE Trans. Power Electron.*, vol. 12, pp. 116–123, Jan. 1997.
- [182] J.-W. Choi and S.-K. Sul, "New current control concept—minimum time current control in 3-phase PWM converter," *IEEE Trans. Power Electron.*, vol. 12, pp. 124–131, Jan. 1997.
- [183] V. Kaura and V. Blasko, "Operation of a voltage source converter at increased utility voltage," *IEEE Trans. Power Electron.*, vol. 12, pp. 132–137, Jan. 1997.
- [184] S. Fukuda, "LQ control of sinusoidal current PWM rectifiers," *Proc. IEE—Elect. Power Applicat.*, vol. 144, pp. 95–100, Mar. 1997.
- [185] Y. Jiang and A. Ekstrom, "General analysis of harmonic transfer through converters," *IEEE Trans. Power Electron.*, vol. 12, pp. 287–293, Mar. 1997.
- [186] L. J. Borle, M. S. Dymond, and C. V. Nayar, "Development and testing of a 20 kW grid interactive photovoltaic power conditioning system in Western Australia," *IEEE Trans. Ind. Applicat.*, vol. 33, pp. 502–508, Mar./Apr. 1997.
- [187] V. Blasko and V. Kaura, "A novel control to actively damp resonance in input LC filter of a three-phase voltage source converter," *IEEE Trans. Ind. Applicat.*, vol. 33, pp. 542–550, Mar./Apr. 1997.
- [188] S. Bhowmik, A. van Zyl, R. Spée, and J. H. R. Enslin, "Sensorless current control for active rectifiers," *IEEE Trans. Ind. Applicat.*, vol. 33, pp. 765–773, May/June 1997.
- [189] J.-J. Shieh, C.-T. Pan, and Z.-J. Cuey, "Modeling and design of a reversible three-phase switching mode rectifier," *Proc. IEE—Elect. Power Applicat.*, vol. 144, pp. 389–396, Nov. 1997.
- [190] M. E. Fraser and C. D. Manning, "True average current predictive controller for four-wire PWM reversible rectifiers," *Proc. IEE—Elect. Power Applicat.*, vol. 144, pp. 397–400, Nov. 1997.
- [191] M. Pande and S. B. Dewan, "Modeling and dynamic analysis of a fast response three phase PWM voltage source rectifier," in *Proc. IEEE Canadian Conf. Electrical and Computer Engineering*, 1998, pp. 677–680.
- [192] R. P. Burgos, E. P. Wiechmann, and J. R. Rodriguez, "A simple adaptive fuzzy logic controller for three-phase PWM boost rectifiers," in *Proc. IEEE ISIE'98*, 1998, pp. 321–326.
- [193] S. M. Ullhaq, E. Hiraki, and M. Nakaoka, "Object-oriented simulation of DSP controlled three phase active PFC converter," in *Proc. IEEE PEVSD'98*, 1998, pp. 28–33.
- [194] Z. Ye, K. Xing, S. Mazumder, D. Borojevic, and F. C. Lee, "Modeling and control of parallel three-phase PWM boost rectifiers in PEBB-based dc distributed power systems," in *Proc. IEEE APEC'98*, vol. 2, 1998, pp. 1126–1132.
- [195] K. Xing, S. Mazumder, Z. Ye, F. C. Lee, and D. Borojevic, "The circulating current in paralleled three-phase boost PFC rectifiers," in *Proc. IEEE PESC'98*, 1998, pp. 783–789.

- [196] J. F. Silva and J. F. Martins, "Space vector alpha beta current regulator for sliding mode controlled unity power factor PWM rectifiers," in *Proc. IEEE IECON'98*, 1998, pp. 1877-1882.
- [197] G. Iuliano, A. L. Schiavo, P. Marino, and A. Testa, "Voltage quality control in a industrial system by means of a three-phase four-wire boost rectifier," in *Proc. IEEE ICHQP'98*, 1998, pp. 107-113.
- [198] A. D. le Roux and J. H. R. Enslin, "Integrated active rectifier and power quality compensator," in *Proc. IEEE ICHQP'98*, 1998, pp. 337-341.
- [199] Y. Suzuki, Y. Iino, K. Kogure, and Y. Kii, "Soft-switching type 3-phase ac input rectifier with high power-factor," in *Proc. IEEE INTELEC'98*, 1998, pp. 470-475.
- [200] J. W. Choi and S. K. Sul, "Fast current controller in three-phase AC/DC boost converter using d-q axis crosscoupling," *IEEE Trans. Power Electron.*, vol. 13, pp. 179-185, Jan. 1998.
- [201] P. Verdelho and G. D. Marques, "DC voltage control and stability analysis of PWM-voltage-type reversible rectifiers," *IEEE Trans. Ind. Electron.*, vol. 45, pp. 263-273, Apr. 1998.
- [202] S. Saetio and D. A. Torrey, "Fuzzy logic control of a space-vector PWM current regulator for three-phase power converters," *IEEE Trans. Power Electron.*, vol. 13, pp. 419-426, May 1998.
- [203] H. Mao, D. Boroyevich, and F. C. Lee, "Novel reduced-order small-signal model of a three-phase PWM rectifier and its application in control design and system analysis," *IEEE Trans. Power Electron.*, vol. 13, pp. 511-531, May 1998.
- [204] C.-T. Pan and J.-J. Shieh, "A family of closed-form duty cycle control laws for three-phase boost AC/DC converter," *IEEE Trans. Ind. Electron.*, vol. 45, pp. 530-543, Aug. 1998.
- [205] H. Kömürçügil and O. Kükrer, "Lyapunov-based control for three-phase PWM AC/DC voltage-source converters," *IEEE Trans. Power Electron.*, vol. 13, pp. 801-813, Sept. 1998.
- [206] P. Verdelho and G. D. Marques, "Four-wire current-regulated PWM voltage converter," *IEEE Trans. Ind. Electron.*, vol. 45, pp. 761-770, Oct. 1998.
- [207] J.-J. Shieh and C.-T. Pan, "ROM-based current controller for three-phase boost-type AC/DC converter," *Proc. IEE—Elect. Power Applicat.*, vol. 145, pp. 544-552, Nov. 1998.
- [208] A. van Zyl, R. Spee, A. Faveluke, and S. Bhowmik, "Voltage sag ride-through for adjustable-speed drives with active rectifiers," *IEEE Trans. Ind. Applicat.*, vol. 34, pp. 1270-1277, Nov./Dec. 1998.
- [209] W.-C. Lee, T.-J. Kweon, D.-S. Hyun, and T.-K. Lee, "A novel control of three-phase PWM rectifier using single current sensor," in *Proc. IEEE PESC'99*, 1999, pp. 515-520.
- [210] J. Wu, H. Dai, K. Xing, F. C. Lee, and D. Boroyevich, "Implementation of a ZCT soft switching technique in a 100 kW PEBB based three-phase PFC rectifier," in *Proc. IEEE PESC'99*, 1999, pp. 647-652.
- [211] S. Mazumder, A. H. Nayfeh, and D. Boroyevich, "New sensorless control of three-phase bi-directional converter using space-vector modulation," in *Proc. IEEE PESC'99*, 1999, pp. 783-788.
- [212] J. Doval-Gandoy, A. Iglesias, C. Castro, and C. M. Peñalver, "Three alternatives for implementing space vector modulation with the dsp TMS320F240," in *Proc. IEEE IECON'99*, 1999, pp. 336-341.
- [213] W.-C. Lee, D.-S. Hyun, and T.-K. Lee, "Single sensor current control of a three-phase voltage-source PWM converter using predictive state observer," in *Proc. IEEE IECON'99*, 1999, pp. 791-796.
- [214] J. Doval-Gandoy, C. Castro, L. Eguizábal, and C. M. Peñalver, "Minimum hardware solution for implementing a complete control algorithm for voltage source rectifiers," in *Proc. IEEE IECON'99*, 1999, pp. 490-495.
- [215] B.-D. Min, J.-H. Youm, and B.-H. Kwon, "SVM-based hysteresis current controller for three-phase PWM rectifier," *Proc. IEE—Elect. Power Applicat.*, vol. 146, pp. 225-230, Mar. 1999.
- [216] T. Ohnishi, O. Miyashita, P. Lataire, and G. Maggetto, "Control of a three-phase PWM rectifier using estimated AC-side and DC-side voltages," *IEEE Trans. Power Electron.*, vol. 14, pp. 222-226, Mar. 1999.
- [217] B. N. Singh, B. Singh, and B. P. Singh, "Fuzzy control of integrated current-controlled converter-inverter-fed cage induction motor drive," *IEEE Trans. Ind. Applicat.*, vol. 35, pp. 405-412, Mar./Apr. 1999.
- [218] K. Jezernik, "VSS control of unity power factor," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 325-332, Apr. 1999.
- [219] A. D. le Roux, J. A. D. Toit, and J. H. R. Enslin, "Integrated active rectifier and power quality compensator with reduced current measurement," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 504-511, June 1999.
- [220] C.-L. Chen, C.-M. Lee, R.-J. Tu, and G.-K. Horng, "A novel simplified space-vector-modulated control scheme for three-phase switch-mode rectifier," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 512-516, June 1999.
- [221] D.-W. Chung and S.-K. Sul, "Minimum-loss strategy for three-phase PWM rectifier," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 517-526, June 1999.
- [222] M.-T. Tsai and W. I. Tsai, "Analysis and design of three-phase AC-to-DC converters with high power factor and near-optimum feed-forward," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 535-543, June 1999.
- [223] H. Kömürçügil and O. Kükrer, "A novel current-control method for three-phase PWM AC/DC voltage-source converters," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 544-553, June 1999.
- [224] J. F. Silva, "Sliding-mode control of boost-type unity-power-factor PWM rectifiers," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 594-603, June 1999.
- [225] J. L. Duarte, A. V. Zwam, C. Wijnands, and C. Vandenput, "Reference frames fit for controlling PWM rectifiers," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 628-630, June 1999.
- [226] J. W. Dixon, J. M. Contardo, and L. A. Moran, "A fuzzy-controlled active front-end rectifier with current harmonic filtering characteristics and minimum sensing variables," *IEEE Trans. Power Electron.*, vol. 14, pp. 724-729, July 1999.
- [227] P. Barras and M. Cade, "PWM rectifier using indirect voltage sensing," *Proc. IEE—Elect. Power Applicat.*, vol. 146, pp. 539-544, Sept. 1999.
- [228] B.-H. Kwon, J.-H. Youm, and J.-W. Lim, "A line voltage-sensorless synchronous rectifier," *IEEE Trans. Power Electron.*, vol. 14, pp. 966-972, Sept. 1999.
- [229] H. S. Song and K. Nam, "Dual current control scheme for PWM converter under unbalanced input voltage conditions," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 953-959, Oct. 1999.
- [230] B.-H. Kwon, J.-H. Youm, J.-W. Lim, K.-W. Seok, and G.-Y. Jeong, "Three-phase PWM synchronous rectifiers without line-voltage sensors," *Proc. IEE—Elect. Power Applicat.*, vol. 146, pp. 632-636, Nov. 1999.
- [231] B. N. Singh, P. Jain, and G. Joos, "Three-phase PWM voltage source rectifier with a reduced number of switches," in *Proc. IEEE CCECE'00*, 2000, pp. 951-955.
- [232] K.-S. Park, S.-C. Ahn, D.-S. Hyun, and S.-Y. Choe, "New control scheme for 3-phase PWM AC/DC converter without phase angle detection under unbalanced input voltage conditions," in *Proc. IEEE APEC'00*, 2000, pp. 501-505.
- [233] S. Hansen, M. Malinowski, F. Blaabjerg, and M. P. Kazmierkowski, "Sensorless control strategies for PWM rectifier," in *Proc. IEEE APEC'00*, 2000, pp. 832-838.
- [234] D.-S. Lee and D.-S. Lim, "AC voltage and current sensorless control of three-phase PWM rectifiers," in *Proc. IEEE PESC'00*, 2000, pp. 588-593.
- [235] R. Zhang, F. C. Lee, and D. Boroyevich, "Four-legged three-phase PFC rectifier with fault tolerant capability," in *Proc. IEEE PESC'00*, 2000, pp. 359-364.
- [236] S. Mobin, E. Hiraki, H. Takano, and M. Nakaoka, "Simulation method for DSP-controlled active PFC high frequency power converters," *Proc. IEE—Elect. Power Applicat.*, vol. 147, pp. 159-166, May 2000.
- [237] D.-C. Lee, G.-M. Lee, and K.-D. Lee, "DC-bus voltage control of three-phase AC/DC PWM converters using feedback linearization," *IEEE Trans. Ind. Applicat.*, vol. 36, pp. 826-833, May/June 2000.
- [238] D. C. Lee, "Advanced nonlinear control of three-phase PWM rectifiers," *Proc. IEE—Elect. Power Applicat.*, vol. 147, pp. 361-366, Sept. 2000.
- [239] S. Chattopadhyay and V. Ramanarayan, "Digital implementation of a line current shaping algorithm for three phase high power factor boost rectifier without input voltage sensing," in *Proc. IEEE APEC'01*, 2001, pp. 592-598.
- [240] S. Chen and G. Joós, "A novel DSP-based adaptive line synchronization system for three-phase utility interface power converters," in *Proc. IEEE PESC'01*, 2001, pp. 528-532.
- [241] S. Chandhakat, M. Yoshida, H. Eiji, M. Nakamura, Y. Konishi, and M. Nakaoka, "Multi-functional digitally-controlled bi-directional interactive three-phase soft-switching PWM converter with resonant snubbers," in *Proc. IEEE PESC'01*, 2001, pp. 589-593.
- [242] R. S. Pena, R. J. Cardenas, J. C. Clare, and G. M. Asher, "Control strategies for voltage control of a boost type PWM converter," in *Proc. IEEE PESC'01*, 2001, pp. 730-735.
- [243] A. V. Stankovic and T. A. Lipo, "A generalized control method for input-output harmonic elimination for the PWM boost rectifier under simultaneous unbalanced input voltages and input impedances," in *Proc. IEEE PESC'01*, 2001, pp. 1309-1314.
- [244] E. Hiraki, M. Yoshida, M. Nakaoka, T. Horiuchi, and Y. Sugawara, "Auxiliary active quasiresonant commutation block snubber-assisted voltage source three phase soft switching PFC rectifier," in *Proc. IEEE PESC'01*, 2001, pp. 1647-1652.

- [245] W. C. Lee, T. K. Lee, and D. S. Hyun, "Comparison of single-sensor current control in the DC link for three-phase voltage-source PWM converters," *IEEE Trans. Ind. Electron.*, vol. 48, pp. 491–505, June 2001.

Unidirectional Buck Converters

- [246] M. Tou, R. Chaffai, K. Al-Haddad, G. Oliver, and V. Rajagopalan, "Analysis and design considerations of unity power factor quasiresonant rectifier," in *Proc. IEEE IECON'93*, 1993, pp. 930–935.
- [247] H. Pouliquen, N. Buchheit, and J. Lethelliez, "Control of a single-switch three-phase rectifier operating in continuous conduction mode," in *Proc. IEEE PEVSD'94*, 1994, pp. 301–306.
- [248] Y. Okuma, S. Igarashi, and K. Kuroki, "Novel three-phase SMR converter with new bilateral switch circuits consisting of IGBT," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1994, pp. 1019–1024.
- [249] B. W. Williams, M. M. Moud, D. Tooth, and S. J. Finney, "A three-phase AC to DC converter with controllable displacement factor," in *Proc. IEEE PESC'95*, 1995, pp. 996–1000.
- [250] F. Andrianoelison, "An alternate solution for AC/DC static conversion with sinusoidal line current," in *Proc. IEEE PESC'95*, 1995, pp. 1178–1182.
- [251] Y. Nidhida, "A new simple topology for three-phase buck-mode PFC rectifier," in *Proc. IEEE APEC'96*, 1996, pp. 531–537.
- [252] D. C. Katsis and F. C. Lee, "A single switch buck converter for hybrid electric vehicle generators," in *Proc. IEEE Power Electronics in Transportation*, 1996, pp. 117–123.
- [253] S. Y. R. Hui, H. Chung, Y. K. E. Ho, and Y. S. Lee, "Modular development of single-stage 3-phase PFC using single-phase step-down converters," in *Proc. IEEE PESC'98*, 1998, pp. 776–782.
- [254] J. Yanchao, W. Fei, Z. Zhen, and T. Ning, "Novel three-phase AC/DC converter without front-end filter," in *Proc. IEEE IECON'98*, 1998, pp. 320–324.
- [255] J. H. Song, Y. D. Kim, I. Choy, and Y. Choi, "A pulse frequency modulation control method for single switch three-phase buck rectifiers," in *Proc. IEEE INTELEC'98*, 1998, pp. 231–236.
- [256] J. Yanchao and S. Mingwei, "A novel three-phase AC/DC converter without front-end filter based on adjustable triangular-wave PWM technique," *IEEE Trans. Power Electron.*, vol. 14, pp. 233–245, Mar. 1999.
- [257] T. Groben, E. Menzel, and J. H. R. Enslin, "Three-phase buck active rectifier with power factor correction and low EMI," *Proc. IEE—Elect. Power Applicat.*, vol. 146, no. 6, pp. 591–596, Nov. 1999.
- [258] K. Wang, D. Boroyevich, and F. C. Lee, "Charge control of three-phase buck PWM rectifiers," in *Proc. IEEE APEC'00*, 2000, pp. 824–831.
- [259] M. Baumann, U. Drogenik, and J. W. Kolar, "New wide input voltage range three-phase unity power factor rectifier formed by integration of a three-switch buck-derived front-end and a DC/DC boost converter output stage," in *Proc. IEEE INTELEC'00*, 2000, pp. 461–470.
- [260] Y. K. E. Ho, S. Y. R. Hui, and Y. S. Lee, "Characterization of single-stage three-phase power-factor-correction circuit using modular single-phase PWM DC-to-DC converters," *IEEE Trans. Power Electron.*, vol. 15, pp. 62–71, Jan. 2000.
- [261] D. J. Tooth, S. J. Finney, and B. W. Williams, "Effects of using DC-side average current-mode control on a three-phase converter with an input filter and distorted supply," *Proc. IEE—Elect. Power Applicat.*, vol. 147, no. 6, pp. 459–468, Nov. 2000.
- [262] S. Kelkar and C. P. Henze, "A high performance three-phase unity power factor rectifier using interleaved buck-derived topology for high power battery charging applications," in *Proc. IEEE PESC'01*, 2001, pp. 1013–1018.
- [263] M. Baumann and J. W. Kolar, "Comparative evaluation of modulation methods for a three-phase/switch buck power factor corrector concerning the input capacitor voltage ripple," in *Proc. IEEE PESC'01*, 2001, pp. 1327–1332.
- [264] T. Kataoka, K. Mizumachi, and S. Miyairi, "A pulsewidth controlled AC-to DC converter to improve power factor and waveform of AC line current," *IEEE Trans. Ind. Applicat.*, vol. IA-15, pp. 670–675, Nov./Dec. 1979.
- [265] T. Ohnishi and H. Okitsu, "A novel PWM technique for three phase inverter/converter," in *Proc. IPEC-Tokyo'83*, 1983, pp. 384–395.
- [266] H. Inaba, S. Shima, A. Ueda, T. Ando, T. Kurosawa, and Y. Sakai, "A new speed control system for DC motors using GTO converter and its applications to elevators," *IEEE Trans. Ind. Applicat.*, vol. IA-21, pp. 391–397, Mar./Apr. 1985.
- [267] P. D. Ziogas, Y. G. Kang, and V. R. Stefanovic, "PWM control techniques for rectifier filter minimization," *IEEE Trans. Ind. Applicat.*, vol. IA-21, pp. 1206–1214, Sept./Oct. 1985.
- [268] ———, "Optimum system design of a three-phase rectifier-inverter type of frequency changer," *IEEE Trans. Ind. Applicat.*, vol. IA-21, pp. 1215–1225, Sept./Oct. 1985.
- [269] E. P. Wiechmann, P. D. Ziogas, and V. R. Stefanovic, "A novel bilateral power conversion scheme for variable frequency static power supplies," *IEEE Trans. Ind. Applicat.*, vol. IA-21, pp. 1226–1233, Sept./Oct. 1985.
- [270] S. R. Doradla, C. Nagamani, and S. Sanyal, "A sinusoidal pulsewidth modulated three-phase AC to DC converter-fed DC motor drive," *IEEE Trans. Ind. Applicat.*, vol. IA-21, pp. 1394–1408, Nov./Dec. 1985.
- [271] M. Hombu, S. Ueda, and A. Ueda, "A current source GTO inverter with sinusoidal inputs and outputs," *IEEE Trans. Ind. Applicat.*, vol. 23, pp. 247–255, Mar./Apr. 1987.
- [272] S. R. Doradla and S. K. Mandal, "A three-phase AC-to-DC power transistor converter-controlled DC motor drive," *IEEE Trans. Ind. Applicat.*, vol. IA-23, pp. 848–854, Sept./Oct. 1987.
- [273] P. Viriya, H. Kubota, and K. Matsuse, "New PWM-controlled GTO converter," *IEEE Trans. Power Electron.*, vol. 2, pp. 373–381, Oct. 1987.
- [274] R. J. Hill and F. L. Luo, "Current source optimization in AC-DC GTO thyristor converters," *IEEE Trans. Ind. Electron.*, vol. 34, pp. 475–482, Nov. 1987.
- [275] Y. Sato and T. Kataoka, "An analysis of voltage spike clamping circuits for current type PWM converters," in *Proc. IEEE PESC'88*, 1988, pp. 813–819.
- [276] S. Nonaka and Y. Neba, "A PWM current source type converter-inverter system for bi-directional power flow," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1988, pp. 296–301.
- [277] P. M. Espelage, J. M. Nowak, and L. H. Walker, "Symmetrical GTO current source inverter for wide speed range control of 2300 to 4160 volt, 350 to 7000 HP, induction motors," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1988, pp. 302–307.
- [278] B. H. Khan, S. R. Doradla, and G. K. Dubey, "A new simultaneous gating GTO dual converter-fed DC motor drive without circulating current," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1988, pp. 520–526.
- [279] S. Fukuda and H. Hasegawa, "Current source rectifier/inverter system with sinusoidal currents," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1988, pp. 909–914.
- [280] P. N. Enjeti, P. D. Ziogas, and J. F. Lindsay, "A current source PWM inverter with instantaneous current control capability," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1988, pp. 927–933.
- [281] J. W. Kolar, H. Ertl, and F. C. Zach, "Analysis of the duality of the three phase PWM converters with DC voltage link and DC current link," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1989, pp. 724–737.
- [282] V. A. Katic and V. S. Vuckovic, "The effect of the new harmonic limitation standards on PWM rectifier AC filter minimization," in *Proc. IEEE MELECON'91*, 1991, pp. 1371–1374.
- [283] B. O. Woo and G. H. Cho, "Soft switching AC/DC/AC converter with current free wheeling circuit," in *Proc. IEEE PESC'91*, 1991, pp. 31–38.
- [284] Y. Sato and T. Kataoka, "State feedback control of current typw PWM AC-to-DC converters," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1991, pp. 927–933.
- [285] B. T. Ooi, B. M. M. Mwinyiwiwa, X. Wang, and G. Joos, "Operating limits of the current-regulated delta-modulated current-source PWM rectifier," *IEEE Trans. Ind. Electron.*, vol. 38, pp. 268–274, Aug. 1991.
- [286] S. Nonaka and Y. Neba, "Quick regulation of sinusoidal output current in PWM converter-inverter system," *IEEE Trans. Ind. Applicat.*, vol. 27, pp. 1045–1062, Nov./Dec. 1991.
- [287] Y. Konishi, N. Arai, K. Kousaka, and S. Kumagai, "A large capacity current source PWM converter with sinusoidal inputs and high power factor," in *Proc. IEEE PESC'92*, 1992, pp. 1361–1367.
- [288] D. Ciscato, L. Malesani, L. Rossetto, P. Tenti, G. L. Basile, M. Pasti, and F. Voelker, "PWM rectifier with low DC voltage ripple for magnet supply," *IEEE Trans. Ind. Applicat.*, vol. 28, pp. 414–420, Mar./Apr. 1992.
- [289] S. Fukuda, Y. Iwaji, and T. Aoyama, "Modeling and control of sinusoidal PWM rectifiers," in *Proc. EPE'93*, 1993, pp. 115–119.
- [290] X. Pierre, J. P. Cambonne, and J. L. Thomas, "A high power voltage source rectifier sinusoidal inputs," in *Proc. EPE'93*, 1993, pp. 120–125.
- [291] Y. Minamoto and T. Ohnishi, "New PWM method for control fed type converter with filter circuit," in *Proc. IEEE PCC-Yokohama'93*, 1993, pp. 67–72.
- [292] F. Leonardi, L. Rossetto, A. Scandellari, R. Speranza, P. Tomasin, P. Tenti, and D. M. Divan, "Soft-switched PWM converter with inductive DC filter," in *Proc. IEEE PCC-Yokohama'93*, 1993, pp. 123–128.
- [293] N. R. Zargari and G. Joos, "A current controlled current-source type unity power PWM rectifier," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1993, pp. 793–799.

Bidirectional Buck Converters

- [294] O. Ojo and I. Bhat, "Analysis of three phase PWM buck rectifier under modulation magnitude and angle control," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1993, pp. 917–925.
- [295] G. Joós and J. Espinoza, "PWM control techniques in current source rectifiers," in *Proc. IEEE IECON'93*, 1993, pp. 1210–1214.
- [296] B.-H. Kwon and B.-D. Min, "A fully software-controller PWM rectifier with current link," *IEEE Trans. Ind. Electron.*, vol. 40, pp. 355–363, June 1993.
- [297] X. Wang and B. T. Ooi, "Unity PF current-source rectifier based on dynamic trilogic PWM," *IEEE Trans. Power Electron.*, vol. 8, pp. 288–294, July 1993.
- [298] —, "Real-time multi-DSP control of three-phase current-source unity power factor PWM rectifier," *IEEE Trans. Power Electron.*, vol. 8, pp. 295–300, July 1993.
- [299] P. N. Enjeti and S. A. Choudhury, "A new control strategy to improve the performance of a PWM AC to DC converter under unbalanced operating conditions," *IEEE Trans. Power Electron.*, vol. 8, pp. 493–500, Oct. 1993.
- [300] A. Insley, N. R. Zargari, and G. Joos, "A neural network controlled unity power factor three phase current source PWM front-end rectifier for adjustable speed drives," in *Proc. IEE PEVDS'94*, 1994, pp. 251–255.
- [301] N. R. Zargari and G. Joos, "A three current-source type PWM rectifier with feed-forward compensation of input displacement factor," in *Proc. IEEE PESC'94*, 1994, pp. 363–368.
- [302] A. Insley, N. R. Zargari, and G. Joós, "A neural network controlled unity power factor three phase PWM rectifier," in *Proc. IEEE PESC'94*, 1994, pp. 577–582.
- [303] A. T. Islier, A. Ersak, and G. Richards, "Harmonic minimization in a three-phase asymmetrical PWM rectifier," in *Proc. IEEE MELECON'94*, 1994, pp. 829–832.
- [304] N. R. Zargari, G. Joos, and P. D. Ziogas, "Input filter design for PWM current-source rectifiers," *IEEE Trans. Ind. Applicat.*, vol. 30, pp. 1573–1579, Nov./Dec. 1994.
- [305] J. Espinoza, G. Joós, and H. Jin, "DSP based space vector PWM pattern generators for PWM current-source rectifiers and inverters," in *Proc. IEEE Canadian Conf. ECE'95*, 1995, pp. 979–982.
- [306] J. R. Espinoza and G. Joos, "A current-source inverter fed induction motor drive system with reduced losses," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1995, pp. 45–52.
- [307] J. Rodríguez and E. Wiechmann, "Control of a high power factor current source converter," in *Proc. IEEE IECON'95*, 1995, pp. 381–386.
- [308] N. A. Rahim, T. C. Green, and B. W. Williams, "Simplified analysis of the three-phase PWM switching converter," in *Proc. IEEE IECON'95*, 1995, pp. 482–487.
- [309] Y. Sato and T. Kataoka, "Simplified control strategy to improve AC-input-current waveform of parallel-connected current-type PWM rectifiers," *Proc. IEEE—Elect. Power Applicat.*, vol. 142, no. 4, pp. 246–254, July 1995.
- [310] D.-C. Lee, D.-H. Kim, and D.-W. Chung, "Control of PWM current source converter and inverter system for high performance induction motor drives," in *Proc. IEEE IECON'96*, 1996, pp. 1100–1105.
- [311] Y. Sato and T. Kataoka, "A current-source PWM rectifier with active damping function," *IEEE Trans. Ind. Applicat.*, vol. 32, pp. 533–554, May/June 1996.
- [312] G. D. Marques, "A current-type PWM rectifier control with active damping based in the space vector technique," in *Proc. IEEE ISIE'97*, 1997, pp. 318–322.
- [313] N. R. Zargari, Y. Xio, and B. Wu, "A near unity input displacement factor PWM rectifier for medium voltage CSI based AC drives," in *Proc. IEEE APEC'97*, 1997, pp. 327–332.
- [314] S. S. Lautaro, Z. H. Francisco, and W. F. Eduardo, "Analysis, design and experimental evaluation of a four-pole PWM rectifier using space vector modulation," in *Proc. IEEE PESC'97*, 1997, pp. 484–490.
- [315] B. J. C. Filho, S. Bernet, and T. A. Lipo, "A new control strategy for the PWM current stiff rectifier/inverter with resonant snubbers," in *Proc. IEEE PESC'97*, 1997, pp. 573–579.
- [316] Y. Konishi, Y. Matsumoto, Y. L. Feng, and M. Nakaoka, "Latest development of current-fed three phase converter with ROM table-based optimum PWM control scheme and its input line filter design," in *Proc. IEEE PESC'97*, 1997, pp. 813–820.
- [317] Y. Konishi, E. Hiraki, Y. Matsumoto, Y. L. Feng, and M. Nakaoka, "Current-fed three-phase converter and voltage-fed three-phase converter using optimum PWM pattern and their performance evaluation," in *Proc. IEEE EPE'97*, vol. 2, 1997, pp. 893–900.
- [318] G. D. Marques and P. Verdelho, "Sliding mode control of a current type PWM rectifier," in *Proc. IEEE EPE'97*, vol. 3, 1997, pp. 349–352.
- [319] Y. Konishi, Y. Matsumoto, P. Boyagoda, and M. Nakaoka, "Current fed three-phase rectifier with optimum PWM-based sine wave line current shaping and unity power factor correction schemes," in *Proc. IEEE IECON'97*, 1997, pp. 888–894.
- [320] J. A. Pomilio, L. Rossetto, P. Tenti, and P. Tomasin, "Performance improvements of soft-switched PWM rectifiers with inductive load," *IEEE Trans. Power Electron.*, vol. 12, pp. 153–1160, Jan. 1997.
- [321] T. C. Green, M. H. Taha, N. A. Rahim, and B. W. Williams, "Three-phase step-down reversible AC-DC power converter," *IEEE Trans. Power Electron.*, vol. 12, pp. 319–324, Mar. 1997.
- [322] J. R. Espinoza and G. Joós, "Current-source converter on-line pattern generator switching frequency minimization," *IEEE Trans. Ind. Electron.*, vol. 44, pp. 198–206, Apr. 1997.
- [323] S.-B. Han, G.-H. Cho, B.-M. Jung, and H.-S. Choi, "Vector-transformed circuit theory and applications to converter modeling/analysis," in *Proc. IEEE PESC'98*, 1998, pp. 538–544.
- [324] G. D. Marques, "A PWM rectifier control system with DC current control based on the space vector modulation and AC stabilization," in *Proc. IEE PEVSD'98*, 1998, pp. 74–79.
- [325] Y. Konishi, M. Ishibashi, and M. Nakaoka, "Three-phase current-source type soft-switched PWM rectifier for high-power applications and its design considerations," in *Proc. IEE PEVSD'98*, 1998, pp. 133–138.
- [326] Y. Konishi, Y. L. Feng, and M. Nakaoka, "Current source three-phase high-power PFC converter with optimum PWM strategy," in *Proc. IEE PEVSD'98*, 1998, pp. 292–298.
- [327] J. R. Espinoza and G. Joos, "State variable decoupling and power flow control in PWM current-source rectifiers," *IEEE Trans. Ind. Electron.*, vol. 45, pp. 78–87, Feb. 1998.
- [328] S.-B. Han, N.-S. Choi, C.-T. Rim, and G.-H. Cho, "Modeling and of static and dynamic characteristics for buck-type three-phase PWM rectifier by circuit DQ transformation," *IEEE Trans. Power Electron.*, vol. 13, pp. 323–336, Mar. 1998.
- [329] Y. Xiao, B. Wu, F. A. D. Winter, and R. Sotudeh, "A dual GTO current-source converter topology with sinusoidal inputs for high power applications," *IEEE Trans. Ind. Applicat.*, vol. 34, pp. 878–884, July/Aug. 1998.
- [330] Y. Xiao, B. Wu, S. C. Rizzo, and R. Sotudeh, "A dual novel power factor control scheme for high power GTO current-source converter," *IEEE Trans. Ind. Applicat.*, vol. 34, pp. 1278–1283, Nov./Dec. 1998.
- [331] Y. Feng, Y. Konishi, and M. Nakaoka, "Deadbeat control scheme for three-phase current-fed PFC converter with an optimum PWM strategy," in *Proc. IEEE PEDS'99*, 1999, pp. 485–488.
- [332] M. Ishibashi, Y. Konishi, Y. L. Feng, and M. Nakaoka, "A novel soft-switched current-fed converter with resonant snubber and current overlapping soft-commutation scheme," in *Proc. IEEE PEDS'99*, 1999, pp. 761–766.
- [333] Y. Konishi and M. Nakaoka, "Current-fed three-phase and voltage-fed three-phase active converters with optimum PWM pattern scheme and their performance evaluations," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 279–287, Apr. 1999.
- [334] N. R. Zargari, Y. Xio, and B. Wu, "Near unity input displacement factor for current source PWM drives," *IEEE Ind. Applicat. Mag.*, vol. 5, pp. 19–25, July/Aug. 1999.
- [335] J. C. Enriquez, J. S. B. Read, J. L. D. Gomez, and J. O. P. Sotelo, "Three-phase six pulse buck rectifier with high quality input waveforms," *Proc. IEEE—Elect. Power Applicat.*, vol. 146, no. 6, pp. 637–645, Nov. 1999.
- [336] T. Halkosaari and H. Tuusa, "Optimal vector modulation of a PWM current source converter according to minimal switching losses," in *Proc. IEEE PESC'00*, 2000, pp. 127–132.
- [337] R. Oruganti and M. Palaniapan, "Extension of inductor voltage control to three-phase buck-type AC-DC converter," *IEEE Trans. Power Electron.*, vol. 15, pp. 295–302, Mar. 2000.
- [338] M. Salo and H. Tuusa, "A vector controlled current source PWM rectifier with a novel current damping method," *IEEE Trans. Power Electron.*, vol. 15, pp. 464–470, May 2000.
- [339] J. D. Gandoy and C. M. Penalver, "Dynamic and steady state analysis of a three phase buck rectifier," *IEEE Trans. Power Electron.*, vol. 15, pp. 953–959, Nov. 2000.
- [340] M. Salo, H. Tuusa, and J. Nyqvist, "A high performance three-phase DC-voltage source—An application to a welding machine," in *Proc. IEEE APEC'01*, 2001, pp. 793–799.
- [341] Y. Ye, M. Kazerani, and V. H. Quintana, "A novel modeling and control method for three-phase PWM converters," in *Proc. IEEE PESC'01*, 2001, pp. 102–107.
- [342] T. Halkosaari, K. Kuusela, and H. Tuusa, "Effect of nonidealities on the performance of the 3-phase current source PWM converter," in *Proc. IEEE PESC'01*, 2001, pp. 654–659.

- [343] J. A. L. Ghijselen, A. P. M. V. D. Bossche, and J. A. A. Melkebeek, "Improved immunity of current sensorless AC-DC converters for mains voltage disturbances," in *Proc. IEEE PESC'01*, 2001, pp. 748–753.
- [344] M. Ishibashi, Y. Konishi, E. Hiraki, and M. Nakaoka, "Three-phase current-fed quasiresonant converter with input side commutation inductors," in *Proc. IEEE PESC'01*, 2001, pp. 1665–1670.
- [345] D. Jeltsema, J. M. A. Scherpen, and J. B. Klaassens, "Energy control of multi-switch power supplies; an application to the three-phase buck type rectifier with input filter," in *Proc. IEEE PESC'01*, 2001, pp. 1831–1836.
- [346] J. R. Espinoza, G. Joos, J. I. Guzman, L. A. Moran, and R. P. Burgos, "Selective harmonic elimination and current/voltage control in current/voltage-source topologies: A unified approach," *IEEE Trans. Ind. Electron.*, vol. 48, pp. 71–81, Feb. 2001.
- [347] D. Graovac and V. Katic, "Online control of current source type active rectifier using transfer function approach," *IEEE Trans. Ind. Electron.*, vol. 48, pp. 526–535, June 2001.

Unidirectional Buck-Boost Converters

- [348] R. Itoh and K. Ishizaka, "Three-phase flyback AC-DC converter with sinusoidal supply currents," *Proc. Inst. Elect. Eng.*, pt. B, vol. 138, no. 3, pp. 143–151, May 1991.
- [349] A. Mechi and S. Funabiki, "Three-phase PWM AC to DC converter with step/down voltage," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1992, pp. 703–709.
- [350] B. Fuld, S. Kern, and R. Ridley, "A combined buck boost power-factor controller for three-phase input," in *Proc. EPE'93*, 1993, pp. 144–148.
- [351] K. Inagaki, T. Furuhashi, A. Ishiguro, M. Ishida, and S. Okuma, "A new PWM control method for ac to dc converters with high-frequency transformer isolation," *IEEE Trans. Ind. Appl.*, vol. 29, pp. 486–492, May/June 1993.
- [352] J. W. Kolar, H. Ertl, and F. C. Zach, "A novel three-phase single-switch discontinuous-mode AC-DC buck-boost converter with high-quality input current waveforms and isolated output," *IEEE Trans. Power Electron.*, vol. 9, pp. 160–172, Mar. 1994.
- [353] C. T. Pan and T. C. Chen, "Step-up/down three-phase AC to DC converter with sinusoidal input current and unity power factor," *Proc. IEEE—Elect. Power Appl.*, vol. 141, no. 2, pp. 77–84, Mar. 1994.
- [354] Y. Nishida, A. Maeda, and H. Tomita, "A new instantaneous-current controller for three-phase buck-boost and buck converters with PFC operation," in *Proc. IEEE APEC'95*, 1995, pp. 875–883.
- [355] L. R. Char, N. Mohan, and C. P. Henze, "Sinusoidal current rectification in a very wide range three-phase AC input to a regulated DC output," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1995, pp. 2341–2347.
- [356] J. C. Salmon, S. Olsen, and E. Nowicki, "A 3-phase buck-boost converter for lowering the input current distortion of a voltage source inverter drive," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1995, pp. 2475–2482.
- [357] H. Oishi, H. Okada, K. Ishizaka, and R. Itoh, "SEPIC-derived three-phase sinusoidal rectifier operating in discontinuous current conduction mode," *Proc. Inst. Elect. Eng.*, pt. B, vol. 142, no. 4, pp. 239–245, July 1995.
- [358] L. Char, G. Kamath, N. Mohan, and C. P. Henze, "Sinusoidal current rectification and ripple cancellation in a very wide three phase AC input to generate a regulated DC output," in *Proc. IEEE PEDES'96*, 1996, pp. 644–648.
- [359] E. H. Ismail and R. Erickson, "Single-switch 3-phase PWM low harmonic rectifiers," *IEEE Trans. Power Electron.*, vol. 11, pp. 338–346, Mar. 1996.
- [360] J. A. Pomilio and G. Spiazzi, "High-precision current source using low-loss, single-switch, three-phase AC/DC converter," *IEEE Trans. Power Electron.*, vol. 11, pp. 561–566, July 1996.
- [361] S. Y. R. Hui and H. Chung, "Paralleling power converters for AC-DC step-down power conversion with inherent power factor correction," in *Proc. EPE'97*, 1997, pp. 1.182–1.187.
- [362] H. Sree, J. H. Kolar, and N. Mohan, "Universal input, unity power factor rectifier using a novel SEPIC-derived topology," in *Proc. EPE'97*, 1997, pp. 2.208–2.213.
- [363] J. W. Kolar, U. Drogenik, and F. C. Zach, "VIENNA rectifier II—A novel single-stage high-frequency isolated three-phase PWM rectifier system," in *Proc. IEEE APEC'98*, 1998, pp. 23–33.
- [364] D. C. Martins, A. H. D. Oliveira, and I. Barbi, "Three-phase rectifier using a SEPIC DC-DC converter in continuous conduction mode for power factor correction," in *Proc. IEEE INTELEC'98*, 1998, pp. 491–497.
- [365] J. Y. Choi, J. P. Lee, I. Choy, and J. H. Song, "A new modular 3-phase AC-DC flyback converter for telecommunication," in *Proc. IEEE INTELEC'98*, 1998, pp. 476–482.
- [366] J. W. Kolar and F. C. Zach, "Direct three-phase single-stage flyback-type power factor corrector," *Electron. Lett.*, vol. 34, p. 1177, June 1998.
- [367] M. L. Heldwein, A. F. D. Souza, and I. Barbi, "A simple control strategy applied to three-phase rectifier units for telecommunication applications using single-phase rectifier modules," in *Proc. IEEE PESC'99*, 1999, pp. 795–800.
- [368] J. Minbock and J. H. Kolar, "Design and experimental investigation of a single-switch three-phase flyback-derived power factor corrector," in *Proc. IEEE INTELEC'00*, 2000, pp. 471–478.
- [369] C. T. Pan and J. J. Shieh, "New space-vector control strategies for three-phase step-up/down AC/DC converter," *IEEE Trans. Ind. Electron.*, vol. 47, pp. 25–35, Feb. 2000.
- [370] M. Baumann, F. Stogerer, J. W. Kolar, and A. Lindemann, "Design of a novel multi-chip power module for a three-phase buck+boost unity power factor utility interface supplying the variable voltage DC link of a square-wave inverter drive," in *Proc. IEEE APEC'01*, 2001, pp. 820–827.
- [371] F. Stogerer, J. Minibock, and J. W. Kolar, "Design and experimental verification of a novel 1.2 kW 480 Vac/24 Vdc two switch three-phase DCM flyback-type unity power factor rectifier," in *Proc. IEEE PESC'01*, 2001, pp. 914–919.
- [372] J. Ying, B. Lu, and J. Zeng, "High efficiency 3-phase input quasi-single-stage PFC-DC/DC converter," in *Proc. IEEE PESC'01*, 2001, pp. 1785–1789.
- [373] D. C. Martins and M. M. Casaro, "Isolated three-phase rectifier with high power factor using Zeta converter in continuous conduction mode," *IEEE Trans. Circuits Syst. I*, vol. 48, pp. 74–80, Jan. 2001.

Bidirectional Buck-Boost Converters

- [374] J. Rodriguez, "A simple control method for a switching rectifier with power transistors," *IEEE Trans. Power Electron.*, vol. 2, pp. 367–372, Oct. 1987.
- [375] S. K. Sul and T. A. Lipo, "Design and performance of a high frequency link induction motor drive operating at unity power factor," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1988, pp. 308–313.
- [376] M. Kazerani, G. Joos, and P. D. Ziogas, "A high performance current controlled regenerative AC-DC converter for DC motor drive," in *Proc. IEEE IECON'90*, 1990, pp. 1169–1175.
- [377] D. G. Himes and T. A. Lipo, "Implementation of a controlled rectifier using AC-AC matrix converter theory," *IEEE Trans. Power Electron.*, vol. 7, pp. 240–250, Jan. 1992.
- [378] V. Vlatkovic and D. Boroyevic, "Digital-signal-processor-based control of three-phase space vector modulated converters," *IEEE Trans. Ind. Electron.*, vol. 41, pp. 326–332, June 1994.
- [379] S. Bernet and R. Teichmann, "The auxiliary resonant commutated pole matrix converter for DC-applications," in *Proc. IEEE PESC'97*, 1997, pp. 1225–1231.
- [380] K. Iimori, K. Shinohra, O. Tarumi, Z. Fu, and M. Muroya, "New current-controlled PWM rectifier-voltage source inverter without DC link components," in *Proc. IEEE PCC-Nagaoka'97*, 1997, pp. 783–786.
- [381] B. Francois and J. P. Cambonne, "Design of logic connection controllers for three-phase voltage-source converters," in *Proc. IEEE ISIE'97*, 1997, pp. 543–548.
- [382] J. B. Ejea, E. Sanchis, A. Ferreres, J. A. Carrasco, and R. D. L. Calle, "High-frequency bi-directional three-phase rectifier based on a matrix converter topology with power factor correction," in *Proc. IEEE APEC'01*, 2001, pp. 828–833.
- [383] J. B. Ejea, E. S. Kilders, J. A. Carrasco, R. D. L. Calle, and J. M. Espi, "High-frequency bi-directional three-phase rectifier with power factor correction," in *Proc. IEEE PESC'01*, 2001, pp. 1303–1308.

Unidirectional Multilevel Converters

- [384] K. Oguchi and Y. Maki, "A multilevel-voltage source rectifier with a three-phase diode bridge circuit as a main power circuit," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1992, pp. 695–702.
- [385] E. L. M. Mehl and I. Barbi, "An improved high power factor and low cost three-phase rectifier," in *Proc. IEEE APEC'95*, 1995, pp. 835–841.
- [386] J. C. Salmon, "3-phase PWM boost rectifier circuit topologies using 2-level and 3-level asymmetrical half-bridges," in *Proc. IEEE APEC'95*, 1995, pp. 842–848.
- [387] H. Mao, F. C. Lee, D. Boroyevich, and S. Hiti, "High performance three-phase power factor correction circuits," in *Proc. IEEE IECON'95*, vol. 1, 1995, pp. 8–14.
- [388] Y. Zhao, Y. Li, and T. A. Lipo, "Force commutated three-level boost type rectifier," *IEEE Trans. Ind. Appl.*, vol. 31, pp. 155–161, Jan./Feb. 1995.

- [389] H. Midavaine, P. L. Moigne, and P. Bartholomeus, "Multilevel three-phase rectifier with sinusoidal input currents," in *Proc. IEEE PESC'96*, 1996, pp. 1595–1599.
- [390] L. Xu and M. Fu, "New current and neutral point voltage control schemes for a boost type three-level rectifier," in *Proc. IEEE PESC'97*, 1997, pp. 491–496.
- [391] D. M. Xu, C. Yang, J. H. Kong, and Z. Qian, "Quasi soft-switching partly decoupled three-phase PFC with approximate unity power factor," in *Proc. IEEE APEC'98*, 1998, pp. 953–957.
- [392] J. Salmon, E. Nowicki, W. Xu, and D. Koval, "Low distortion 3-phase rectifiers utilizing harmonic correction circuit topologies with both IGBT and thyristor switches," in *Proc. IEEE APEC'98*, 1998, pp. 1100–1106.
- [393] —, "Optimizing the performance of a 3-phase diode rectifier using 3-level harmonic correction circuits with low switching frequencies," in *Proc. IEEE APEC'98*, 1998, pp. 1107–1113.
- [394] P. Ide, N. Froehleke, H. Grotstollen, W. Korb, and B. Margaritis, "Operation of a three-phase/three-level-rectifier in wide range and single-phase applications," in *Proc. IEEE IECON'99*, vol. 2, 1999, pp. 577–582.
- [395] C. M. T. Cruz and I. Barbi, "A passive lossless snubber for the high power factor unidirectional three-phase three-level rectifier," in *Proc. IEEE IECON'99*, vol. 2, 1999, pp. 909–914.
- [396] P. Ide, N. Froehleke, H. Grotstollen, W. Korb, and B. Margaritis, "Analysis of a three-phase/three-level-rectifiers at low load and discontinuous conduction mode," in *Proc. IEEE APEC'00*, vol. 1, 2000, pp. 197–204.
- [397] M. D. Manjrekar, P. K. Steimer, and T. A. Lipo, "Hybrid multilevel power conversion system: A competitive solution for high-power applications," *IEEE Trans. Ind. Applicat.*, vol. 36, pp. 834–841, May/June 2000.
- [398] J. Minibock and J. W. Kolar, "Comparative theoretical and experimental evaluation of bridge leg topologies of a three-phase three-level utility power factor rectifier," in *Proc. IEEE PESC'01*, 2001, pp. 1641–1646.
- [399] P. Barbosa, F. Canales, and F. Lee, "Analysis and evaluation of the two-switch three-level boost rectifier," in *Proc. IEEE PESC'01*, 2001, pp. 1659–1664.
- [400] D. Carlton and W. G. Dunford, "Multilevel, unidirectional AC-DC converters, a cost effective alternative to bi-directional converters," in *Proc. IEEE PESC'01*, 2001, pp. 1911–1917.
- Bidirectional Multilevel Converters**
- [401] L. H. Walker, "10 MW GTO converter for battery peaking services," *IEEE Trans. Ind. Applicat.*, vol. 26, pp. 63–72, Jan./Feb. 1990.
- [402] A. Mahfouz, J. Holtz, and A. E.A. El. Tobshy, "Development of an integrated high voltage 3-level converter-inverter system with sinusoidal input-output for feeding 3-phase induction motors," in *Proc. EPE'93*, 1993, pp. 134–139.
- [403] F. Z. Peng, J. S. Lai, J. McKeever, and J. VanCoevering, "A multilevel voltage-source converter system with balanced DC voltage," in *Proc. IEEE PESC'95*, 1995, pp. 1144–1150.
- [404] J. S. Lai and F. Z. Peng, "Multilevel converters—A new breed of power converters," *IEEE Trans. Ind. Applicat.*, vol. 32, pp. 509–517, May/June 1996.
- [405] S. Fukuda and Y. Matsumoto, "Neutral point potential and unity power factor control of NPC boost converters," in *Proc. IEEE PCC-Nagaoka'97*, 1997, pp. 231–236.
- [406] K. Oguchi, N. Nakajima, and T. Sano, "Three-phase three-level voltage source converters coupled with harmonic canceling interphase reactors," in *Proc. EPE'97*, 1997, pp. 4.162–4.167.
- [407] P. G. Kamp, B. Endres, and M. Wolf, "Modular high power DC-link converter units for power system application," in *Proc. EPE'97*, 1997, pp. 4.305–4.310.
- [408] K. Oguchi, H. Hama, and T. Kubota, "Multilevel current-source and voltage source converter systems coupled with harmonic canceling reactors," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1997, pp. 1300–1308.
- [409] S. Fukuda and Y. Matsumoto, "Optimal regulator based control of NPC boost rectifiers for unity power factor and reduced neutral point potential variations," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1997, pp. 1455–1462.
- [410] N. R. Zargari, Y. Xiao, and B. Wu, "A multilevel thyristor rectifier with improved power factor," *IEEE Trans. Ind. Applicat.*, vol. 33, pp. 1208–1213, Sept./Oct. 1997.
- [411] B. Mwinjiwiwa, Z. Wolanski, Y. Chen, and B. T. Ooi, "Multimotor multilevel converters with input/output lineary," *IEEE Trans. Ind. Applicat.*, vol. 33, pp. 1214–1219, Sept./Oct. 1997.
- [412] E. M. Berkouk and G. Manesse, "Multilevel PWM rectifier-multilevel inverter cascade application to the speed control of the PMSM," in *Proc. IEEE ICCE'98*, 1998, pp. 1031–1035.
- [413] M. L. Tolbert and F. Z. Peng, "Multilevel converters for large electric drives," in *Proc. IEEE APEC'98*, 1998, pp. 530–536.
- [414] G. Sinha and T. A. Lipo, "A four-level rectifier-inverter system for drive applications," *IEEE Ind. Applicat. Mag.*, vol. 4, pp. 66–74, Jan./Feb. 1998.
- [415] Y. Chen and B. T. Ooi, "Multimodular multilevel rectifier/inverter link with independent reactive power control," *IEEE Trans. Power Delivery*, vol. 13, pp. 902–908, July 1998.
- [416] N. P. Schibli, T. Nguyen, and A. C. Rufer, "A three-phase multilevel converter for high-power induction motors," *IEEE Trans. Power Electron.*, vol. 13, pp. 978–986, Sept. 1998.
- [417] D. Zhou and D. Rouaud, "Experimental comparisons of space vector neutral point balancing strategies for three-level topology," in *Proc. IEEE PESC'99*, 1999, pp. 1071–1076.
- [418] J. Rodriguez, L. Moran, A. Gonzalez, and C. Silva, "High voltage multilevel converter with regeneration capability," in *Proc. IEEE PESC'99*, 1999, pp. 1077–1082.
- [419] L. Wei, F. Li, and C. Li, "A direct power feedback method of a dual PWM three-level voltage source converter system," in *Proc. IEEE PESC'99*, 1999, pp. 1089–1094.
- [420] E. C. Nho, I. D. Kim, and T. A. Lipo, "A new boost type rectifier for a DC power supply with frequent output short circuit," in *Conf. Rec. IEEE-IAS Annu. Meeting*, vol. 2, 1999, pp. 1165–1172.
- [421] H. Gheraia, E. M. Berkouk, and G. Manesse, "Feedback control of the input DC voltage sources of the seven levels NPC voltage source inverter," in *Proc. IEEE AFRICON'99*, 1999, pp. 691–696.
- [422] G. Walker and G. Ledwich, "Bandwidth considerations for multilevel converters," *IEEE Trans. Power Electron.*, vol. 14, pp. 74–81, Jan. 1999.
- [423] L. M. Tolbert, F. Z. Peng, and T. G. Habetler, "Multilevel converters for large electric drives," *IEEE Trans. Ind. Applicat.*, vol. 35, pp. 36–44, Jan./Feb. 1999.
- [424] S. Fukuda, Y. Matsumoto, and A. Sagawa, "Optimal-regulator-based control of NPC boost rectifiers for unity power factor and reduced neutral-point-potential variations," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 527–534, June 1999.
- [425] Y. Chen, B. Mwinjiwiwa, Z. Wolanski, and B. T. Ooi, "Unified power flow controller (UPFC) based on chopper stabilized diode-clamped multilevel converters," *IEEE Trans. Power Electron.*, vol. 15, pp. 258–267, Mar. 2000.
- [426] T. Ishida, K. Matsuse, K. Sugita, L. Huang, and K. Sasagawa, "DC voltage control strategy for a five-level converter," *IEEE Trans. Power Electron.*, vol. 15, pp. 508–515, May 2000.
- [427] O. A. Sadaba, P. S. Gurrupide, J. L. Taberna, I. M. Morales, and L. M. Palomo, "Voltage harmonics generated by 3-level converters using PWM natural sampling," in *Proc. IEEE PESC'01*, 2001, pp. 1561–1565.
- [428] L. Xu and V. G. Agelidis, "A flying capacitor multilevel PWM converter based UPFC," in *Proc. IEEE PESC'01*, 2001, pp. 1905–1910.
- [429] K. T. Wong, "Harmonic analysis of PWM multilevel converters," *Proc. IEEE—Elect. Power Applicat.*, vol. 148, pp. 35–43, Jan. 2001.
- [430] N. Celanovic and D. Boroyevich, "A fast space-vector modulation algorithm for multilevel three-phase converters," *IEEE Trans. Ind. Applicat.*, vol. 37, pp. 637–641, Mar./Apr. 2001.
- Unidirectional Multipulse Converters**
- [431] B. M. Bird, J. F. Marsh, and P. R. McLellan, "Harmonic reduction in multiplex converters by triple-frequency current injection," *Proc. Inst. Elect. Eng.*, vol. 116, no. 10, pp. 1730–1734, Oct. 1969.
- [432] E. J. Cham and T. Specht, "The ANSI 49 rectifier with phase shift," *IEEE Trans. Ind. Applicat.*, vol. IA-20, pp. 615–624, May/June 1984.
- [433] R. Hammond, L. Johnson, A. Shimp, and D. Harder, "Magnetic solutions to line current harmonic reduction," in *Proc. PCIM (USA)'94*, 1994, pp. 354–364.
- [434] S. Kim, P. N. Enjeti, P. Packebush, and I. J. Pitel, "A new approach to improve power factor and reduce harmonics in a three-phase diode rectifier type utility interface," *IEEE Trans. Ind. Applicat.*, vol. 30, pp. 1557–1564, Nov./Dec. 1994.
- [435] B. S. Lee, P. N. Enjeti, and I. J. Pitel, "A new 24-pulse diode rectifier system for AC motor drives provides clean power utility interface with low kVA components," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1996, pp. 1024–1031.
- [436] S. Choi, P. N. Enjeti, and I. J. Pitel, "Polyphase transformer arrangements with reduced kVA capacities for harmonic current reduction in rectifier-type utility interface," *IEEE Trans. Power Electron.*, vol. 11, pp. 680–690, Sept. 1996.

- [437] S. Choi, P. N. Enjeti, H. H. Lee, and I. J. Pitel, "A new active interphase reactor for 12-pulse rectifiers provides clean power utility interface," *IEEE Trans. Ind. Appl.*, vol. 32, pp. 1304–1311, Nov./Dec. 1996.
- [438] V. F. Pires and J. F. Silva, "A new topology for paralleling three phase half wave rectifiers with a high power factor and sinusoidal input currents," in *Proc. EPE'97*, 1997, pp. 2.238–2.243.
- [439] S. Masukawa and S. Iida, "An improved three-phase diode rectifier for reducing AC line current harmonics," in *Proc. EPE'97*, 1997, pp. 4.227–4.232.
- [440] Y. Nishida and M. Nakaoka, "A new harmonic reducing three-phase diode rectifier for high voltage and high power applications," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1997, pp. 1624–1632.
- [441] K. Oguchi and T. Yamada, "Novel 18-step diode rectifier circuit with nonisolated phase shifting transformers," *Proc. IEEE-Electric Power Applications*, vol. 144, no. 1, pp. 1–5, Jan. 1997.
- [442] S. Choi, B. S. Lee, and P. N. Enjeti, "New 24-pulse diode rectifier systems for utility interface of high-power AC motor drives," *IEEE Trans. Ind. Appl.*, vol. 33, pp. 531–541, Mar./Apr. 1997.
- [443] J. P. G. Abreu, C. A. M. Guimaraes, G. Paulillo, and R. A. Oliveira, "A power converter autotransformer," in *Proc. IEEE HQPP'98*, vol. 2, 1998, pp. 1059–1063.
- [444] D. Rendusara, K. J. Slater, B. S. Lee, and P. Enjeti, "Design consideration for 12/24-pulse auto-connected rectifiers for large VA, PWM drive systems," in *Proc. IEEE APEC'99*, 1999, pp. 903–909.
- [445] S. Choi, J. Oh, K. Kim, and J. Cho, "A new 24-pulse diode rectifier for high voltage and high power applications," in *Proc. IEEE PESC'99*, 1999, pp. 169–174.
- [446] K. Oguchi, G. Maeda, N. Hoshi, and T. Kubota, "Voltage-phase shifting effect of three-phase harmonic canceling reactors and their applications to three-level diode rectifiers," in *Conf. Rec. IEEE-IAS Annu. Meeting*, vol. 2, 1999, pp. 796–803.
- [447] Y. Nishida, "A 12-pulse diode rectifier using 3-phase bridge 6-pulse diode rectifier with 2 additional diodes and an auto-transformer," in *Proc. IEEE PEDS'99*, 1999, pp. 75–79.
- [448] G. N. Bathurst, B. C. Smith, N. R. Watson, and J. Arrillaga, "Harmonic domain modeling of high-pulse converters," *Proc. IEEE-Electric Power Applications*, vol. 146, no. 3, pp. 335–340, May 1999.
- [449] B. S. Lee, J. Hahn, P. N. Enjeti, and I. J. Pitel, "A robust three-phase active power-factor-correction and harmonic reduction scheme for high power," *IEEE Trans. Ind. Electron.*, vol. 46, pp. 483–494, June 1999.
- [450] J. Hahn, M. Kang, P. N. Enjeti, and I. J. Pitel, "Analysis and design of harmonic subtractors for three phase rectifier equipment to meet compliance," in *Proc. IEEE APEC'00*, 2000, pp. 211–217.
- [451] P. Pejovic and Z. Janda, "An improved current injection network for three-phase high-power-factor rectifiers that apply the third harmonic current injection," *IEEE Trans. Ind. Electron.*, vol. 47, pp. 497–499, Apr. 2000.
- [452] C. L. Chen and G. H. Hornig, "A new passive 28-step current shaper for three-phase rectification," *IEEE Trans. Ind. Electron.*, vol. 47, pp. 1212–1219, Dec. 2000.
- [453] S. Hansen, S. Uffe, and F. Blaabjerg, "Quasi 12-pulse rectifier for adjustable speed drives," in *Proc. IEEE APEC'01*, 2001, pp. 806–812.
- [454] G. Ivensky and S. B. Yaakov, "A novel three-phase rectifier with reduced THD," in *Proc. IEEE PESC'01*, 2001, pp. 672–677.
- [455] F. J. M. D. Seixas and I. Barbi, "A new three-phase low THD power supply with high frequency isolation and 60 V/200 A regulated DC output," in *Proc. IEEE PESC'01*, 2001, pp. 1629–1634.
- [461] ———, "Pulse doubling in parallel convertor configurations with inter-phase reactors," *Proc. Inst. Elect. Eng.*, pt. B, vol. 138, no. 1, pp. 15–20, Jan. 1991.
- [462] ———, "24-pulse HVDC conversion," *Proc. Inst. Elect. Eng.*, pt. C, vol. 138, no. 1, pp. 57–64, Jan. 1991.
- [463] ———, "Pulse multiplication in parallel convertors by multitrapp control of interphase reactors," *Proc. Inst. Elect. Eng.*, pt. B, vol. 139, no. 1, pp. 13–20, Jan. 1992.
- [464] M. Villablanca and J. Arrillaga, "Single-bridge unit-connected HVDC generation with increased pulse number," *IEEE Trans. Power Delivery*, vol. 8, pp. 681–687, Apr. 1993.
- [465] S. Choi, P. N. Enjeti, and I. J. Pitel, "Autotransformer configurations to enhance utility power quality of high power AC/DC rectifier systems," in *Proc. Particle Accelerator Conf. '95*, 1995, pp. 1985–1987.
- [466] C. Guimaraes, G. Olivier, and G.-E. April, "High current AC/DC power converters using T-connected transformers," in *Proc. IEEE Canadian Conf. ECE'95*, 1995, pp. 704–707.
- [467] G. Olivier, G. E. April, E. Ngandui, and C. Guimaraes, "Novel transformer connection to improve current sharing in high-current DC rectifiers," *IEEE Trans. Ind. Appl.*, vol. 31, pp. 127–133, Jan./Feb. 1995.
- [468] V. F. Pires, J. F. Silva, and A. Anunciada, "Twelve pulse parallel rectifier with a new topology for the output low-pass filter," in *Proc. IEEE PESC'96*, 1996, pp. 1006–1011.
- [469] T. Tanaka, N. Koshio, and H. Akagi, "A novel method of reducing the supply current harmonics of a 12 pulse thyristor rectifier with an interphase reactor," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1996, pp. 1256–1262.
- [470] S. F. Pinto and J. F. Silva, "Voltage control of twelve pulse rectifiers fitted with double LC filters," in *Proc. IEEE ISIE'97*, 1997, pp. 323–328.
- [471] E. Wiechmann, R. Burgos, and J. Rodriguez, "Staggered phase controlled rectifier: A novel structure to achieve high power factor," in *Proc. IEEE PESC'97*, 1997, pp. 821–827.
- [472] ———, "High power factor phase controlled rectifier using staggered converters," in *Conf. Rec. IEEE-IAS Annu. Meeting*, 1997, pp. 1390–1397.
- [473] Y. B. Blauth and I. Barbi, "A phase-controlled 12-pulse rectifier with unity displacement factor without phase shifting transformer," in *Proc. IEEE APEC'98*, vol. 2, 1998, pp. 970–976.
- [474] T. Tanaka, N. Koshio, H. Akagi, and A. Nabae, "Reducing supply current harmonics," *IEEE Ind. Appl. Mag.*, vol. 4, pp. 31–37, Sept./Oct. 1998.
- [475] G. Paulillo, J. Policarpo, G. Abreu, C. A. M. Guimaraes, and R. A. Oliveira, "T-ADZ—a novel converter transformer," in *Proc. IEEE ICHQP'00*, 2000, pp. 715–719.
- [476] M. Villablanca, J. D. Valle, J. Rojas, and W. Rojas, "A modified back-to-back HVdc system for 36-pulse operation," *IEEE Trans. Power Delivery*, vol. 15, pp. 641–645, Apr. 2000.
- [477] S. Choi and J. Jung, "New pulse multiplication technique based on 6-pulse thyristor converters for high power applications," in *Proc. IEEE APEC'01*, 2001, pp. 800–805.

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- [456] J. Arrillaga, A. P. B. Joosten, and J. F. Baird, "Increasing the pulse number of AC-DC converters by current rejection techniques," *IEEE Trans. Power App. Syst.*, vol. PAS-102, pp. 2649–2655, Aug. 1983.
- [457] G. E. April and G. Olivier, "A novel type of 12-pulse converter," *IEEE Trans. Ind. Appl.*, vol. IA-21, pp. 180–191, Jan./Feb. 1985.
- [458] S. Miyairi, S. Iida, K. Nakata, and S. Masukawa, "New method for reducing harmonics involved in input and output of rectifier with interphase transformer," *IEEE Trans. Ind. Appl.*, vol. IA-22, pp. 790–797, Sept./Oct. 1986.
- [459] S. Arabi, M. Z. Tarnawcky, and M. R. Iravani, "Dynamic performance of an HVDC 24-pulse series tapping station," *IEEE Trans. Power Delivery*, vol. 3, pp. 2112–2118, Oct. 1988.
- [460] J. Arrillaga and M. Villablanca, "A modified parallel HVDC converter for 24 pulse operation," *IEEE Trans. Power Delivery*, vol. 6, pp. 231–237, Jan. 1991.



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