# Adaptive Repetitive Control of PWM Inverters for Very Low THD AC-Voltage Regulation with Unknown Loads

Ying-Yu Tzou, Member, IEEE, Shih-Liang Jung, Student Member, IEEE, and Hsin-Chung Yeh

Abstract—An adaptive repetitive control scheme is proposed and applied to the control of a pulsewidth-modulated (PWM) inverter used in a high-performance ac power supply. The proposed control scheme can adaptively eliminate periodic distortions caused by unknown periodic load disturbances in an ac power supply. The proposed adaptive repetitive controller consists of a voltage regulator using state feedback control, a repetitive controller with tuning parameters, and an adaptive controller with a recursive least-squares estimator (LSE). This adaptive repetitive controller designed for ac-voltage regulation has been realized using a single-chip digital signal processor (DSP) TMS320C14 from Texas Instruments. Experimental verification has been carried out on a 2-kVA PWM inverter. Simulation and experimental results show that the DSP-based adaptive repetitive controller can achieve both good dynamic response and low total harmonic distortion (THD) under large-load disturbances and uncertainties.

*Index Terms*—AC power supply, adaptive repetitive control, DSP control, pulsewidth-modulated inverter.

# I. INTRODUCTION

**C**LOSED-LOOP-regulated pulsewidth-modulated (PWM) inverters have been widely applied in various types of ac power-conditioning systems, such as uninterruptible power supply (UPS) systems, automatic voltage regulators (AVR's), and programmable ac sources (PAS's). One major requirement of these applications is that the system is required to maintain a low-distortion waveform under transient or periodic load disturbances. Some research has examined the closed-loop regulation of PWM inverters to achieve good dynamic response and most of them have focused on transient response improvement through instantaneous feedback control [1]–[7]. Although satisfactory results have been obtained for transient load disturbances, periodic disturbance is cyclic by nature.

Repetitive control theory [8]–[10] originating from the internal model principle [11] provides a solution for eliminating periodic errors which occur in a dynamic system. A repetitive controller can be viewed as a periodic waveform generator

H.-C. Yeh is with Afreey, Inc., 10075 Taipei, Taiwan, R.O.C.

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augmented within the control loop of a control system, which is closed-loop regulated by a feedback controller, so that the periodic errors can be eliminated. A number of repetitive control schemes have been developed and applied to various industrial applications [12]–[14]. Haneyoshi *et al.* [15] and Nishida and Haneyoshi [16] have applied the repetitive control technique to eliminate periodic distortions in a PWM inverter. In their approaches, the repetitive controller was designed based on the exact model of the closed-loop-controlled system with one-step prediction. Therefore, the performance and stability of such a repetitive control system is highly dependent upon the robustness of the original feedback control system.

To relax the stringent stability requirement of a repetitive control system, a modified repetitive control scheme with finite frequency mode elimination has been developed [17]. However, the convergence of such a system will deteriorate due to plant uncertainties. For time-varying systems or systems with large plant uncertainties, adaptive repetitive control schemes were developed to eliminate periodic errors [18], [19]. Although these methods can track the changing plant dynamics, they have the drawback that the number of parameters to be estimated is proportional to the frequency modes selected to be cancelled.

In this paper, we have developed a new adaptive repetitive control scheme that employs an auxiliary compensator to stabilize the closed-loop system, and its parameters are tuned by an adaptive tuning controller which recursively on line identifies the plant dynamics. The adaptive repetitive controller will guarantee the closed-loop stability under plant variations and, at the same time, eliminate periodical errors resulting from all frequency modes below the closed-loop bandwidth.

Section II introduces the operational principle. The stability criterion and convergence of the periodic error of a repetitive control system are also derived. Section III describes the proposed adaptive repetitive control scheme in detail. Section IV describes the modeling and the DSP-based repetitive control of a PWM inverter for ac-voltage regulation. Section V illustrates the simulation and experimental verification of the proposed control scheme, and Section VI gives the conclusion.

#### II. REPETITIVE CONTROL SYSTEM

### A. Principle of Repetitive Control

A servomechanism system is required to regulate the controlled variables to follow the corresponding reference

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Y.-Y. Tzou and S.-L. Jung are with the Department of Electrical and Control Engineering, National Chiao Tung University, 30050 Hsinchu, Taiwan, R.O.C.



Fig. 1. Configurations of repetitive control systems.

commands without steady-state error in the presence of unknown and unmeasurable disturbance inputs. In servomechanism system design, the internal model principle proposed by Francis and Wonham [11] plays an important role. The internal model principle states that the controlled output tracks a set of reference inputs without steady-state error if the model which generates these references is included in the stable closedloop system. The internal model principle therefore reveals that high-accuracy, asymptotic, and tracking properties for periodic exogenous inputs can be achieved by locating the model, which generates a set of periodic signals, within the control loop. Repetitive control is a control scheme in which the frequency modes of the periodic error to be eliminated are included in the stable control loops. Repetitive control can be easily realized using a microprocessor-based digital controller. With the advance of high-performance microprocessors and digital signal processors (DSP's), more frequency modes can be included in the control loops. This reveals the feasibility of implementation of ultraprecision servomechanism systems.

In a repetitive control system, a repetitive controller is inserted in the control loop in addition to the conventional tracking controller. There are various control configurations for the repetitive control systems. Fig. 1 shows two basic control structures of the repetitive control system. Fig. 1(a) shows a cascaded-type repetitive controller, in which it is cascaded as an outer loop controller. Fig. 1(b) provides a feedforward path to the repetitive controller. The major purpose of the tracking controller is to improve the system transient response so that it is insensitive to external disturbances, while the repetitive controller is designed to reduce periodic errors caused by periodic references or disturbances.

Fig. 2 shows the block diagram of the proposed repetitive control system, where  $P(z^{-1})$  represents the closed-loop transfer function of the plant which is closed-loop regulated by a tracking controller,  $S(z^{-1})$  and  $Q(z^{-1})$  are the auxiliary



Fig. 2. Block diagram of a discrete-time repetitive control system.

compensators of the repetitive controller, r(k) is the reference signal, y(k) is the system output, e(k) is the tracking error, and  $r_c(k)$  is the compensated reference command.

The transfer function from the disturbance input d(k) to the tracking error e(k) in Fig. 2 is

$$H(z^{-1}) \equiv \frac{E(z^{-1})}{D(z^{-1})}$$
  
=  $\frac{1}{1 + \frac{z^{-N}}{1 - Q(z^{-1})z^{-N}} S(z^{-1})P(z^{-1})}$  (1)

where  $E(z^{-1})$  and  $D(z^{-1})$  are z transforms of e(k) and d(k). The corresponding frequency response in s domain is

$$H(j\omega) = H(z^{-1})|_{z=e^{j\omega T}}.$$
(2)

If d(k) is a periodic disturbance with period of N, then the Fourier series representation of d(k) can be expressed as

$$d(k) = \sum_{n=0}^{N-1} c_n e^{j2\pi kn/N}$$
(3)

where  $c_n$  denotes the Fourier coefficients. In a special case, if  $Q(z^{-1}) = 1$  and  $P(z^{-1})$  is stable, we can obtain

$$|H(j\omega)| = 0$$
 at  $\omega = 2n\pi/N$ ,  $n = 0, \dots, N-1$ . (4)

This reveals that these frequency modes of the periodic error have been eliminated by the repetitive controller, and perfect tracking can thus be achieved under such a condition. However, perfect tracking imposes a stringent stability requirement in the synthesis of  $S(z^{-1})$ . In practice, we can relax this requirement by choosing  $Q(z^{-1})$ , a low-pass filter, or a constant less than unity such that

$$|H(j\omega)| < \mu(j\omega)$$
 at  $\omega = 2n\pi/N$ ,  $n = 0, \cdots, N-1$ 
(5)

where  $\mu(j\omega)$  specifies the attenuation factor of each frequency mode of the periodic disturbance.

#### B. Stability Analysis

From Fig. 2, we can find

$$E(z^{-1}) = R(z^{-1}) - Y(z^{-1})$$
(6)



Fig. 3. Block diagram representation of the error convergence.

and

$$Y(z^{-1}) = P(z^{-1}) \bigg\{ R(z^{-1}) + E(z^{-1}) \\ \cdot \frac{z^{-N}}{1 - Q(z^{-1})z^{-N}} S(z^{-1}) \bigg\}.$$
 (7)

Eliminating  $Y(z^{-1})$  from (6) and (7), we can get

$$E(z^{-1}) = E(z^{-1})z^{-N}(Q(z^{-1}) - P(z^{-1})S(z^{-1})) + (1 - P(z^{-1}))(1 - Q(z^{-1})z^{-N})R(z^{-1}).$$
 (8)

Fig. 3 shows the block diagram representation of (8), and we can observe that if

$$|Q(z^{-1}) - P(z^{-1})S(z^{-1})|_{z=e^{j\omega T}} < 1, \quad \text{for all } \omega \quad (9)$$

and  $P(z^{-1})$  is stable and then e(k) is bounded which implies that the system is stable.

The design of  $S(z^{-1})$  and  $Q(z^{-1})$  is a compromise between the relative stability and the convergence rate of the periodic error. For simplicity, if we choose  $Q(z^{-1})$  to be a constant less than and close to unity, we can arbitrarily choose an  $S(z^{-1})$  with very small gain to satisfy the stability criterion of (9). However, the periodic error may still be large. To satisfy both requirements (5) and (9), we can choose  $Q(z^{-1})$ to be a constant less than and close to unity and  $S(z^{-1})$  to be a digital filter with phase-lead characteristics. An optimal choice of  $S(z^{-1})$  in terms of large relative stability and fast convergence rate can be achieved when  $S(z^{-1})P(z^{-1})$ possesses a nearly zero-phase-shift characteristic [19]. This can be accomplished by choosing  $S(z^{-1})$  as the inverse of  $P(z^{-1})$  and augmented with a realizable low-pass filter with appropriate cutoff frequency.

# C. Convergence Analysis

The  $|Q(z^{-1}) - S(z^{-1})P(z^{-1})|$  in the stability criterion of (9) can also be viewed as a performance index for the convergence of periodic error. A smaller  $|Q(z^{-1}) - S(z^{-1})P(z^{-1})|$  results in a faster error convergence. The convergence index is defined as

$$h \equiv |Q(z^{-1}) - P(z^{-1})S(z^{-1})|_{\infty}.$$
 (10)

If h = 0, the periodic error concerned can be eliminated after one cycle. However, such a condition requires a perfect match of the plant model  $P(z^{-1})$ , and this is obviously unrealistic. To avoid such situation, we define

$$S(z^{-1}) = \frac{g}{s\tau + 1} \frac{Q(z^{-1})}{P(z^{-1})}$$
(11)



Fig. 4. The proposed adaptive repetitive control scheme.

where g is a constant between zero and unity and  $\tau$  is a realization time constant.

# III. ADAPTIVE REPETITIVE CONTROL SYSTEM

Fig. 4 shows the proposed adaptive repetitive control scheme. In addition to the conventional repetitive controller, an adaptive parameter tuner is included in the control loop to adjust the control parameters of  $S(z^{-1})$  according to the identified plant dynamics. Let  $P(k, z^{-1})$  be the transfer function of a second-order time-varying system and represented by

$$P(k, z^{-1}) = \frac{z^{-2}}{1 + a_1(k)z^{-1} + a_2(k)z^{-2}}$$
(12)

where the parameters  $a_1(k)$  and  $a_2(k)$  are left to be identified.

There are a number of well-known parameter estimation techniques that have been successfully applied to the identification problem [20]. The recursive least-squares estimator (LSE) has the advantages of being unbiased, fast convergent, and can be applicable to a wide range of applications in which other statistical estimation theories may be difficult to apply [21]. In the application of an adaptive control system, the most important factor to be considered is whether the designed control law can be realized with an acceptable sampling rate. The recursive LSE (RLSE) parameter identification algorithm used in this paper is

$$\hat{\theta}(k) = \hat{\theta}(k-1) + \frac{P(k-2)\phi(k-1)}{\alpha + \phi(k-1)'P(k-2)\phi(k-1)} \cdot (y(k) - r_c(k-2) - \phi(k-1)'\hat{\theta}(k-1))$$
(13)

$$P(k-1) = \frac{1}{\alpha} \left[ P(k-2) - \frac{P(k-2)\phi(k-1)\phi(k-1)'P(k-2)}{\alpha + \phi(k-1)'P(k-2)\phi(k-1)} \right]$$
(14)

 $\hat{\theta}(k-1) = \begin{bmatrix} a_1(k) & a_2(k) \end{bmatrix}'$ (15)

$$\phi(k-1) = \begin{bmatrix} -y(k-1) & -y(k-2) \end{bmatrix}'$$
(16)



Fig. 5. Parameter identification of a PWM inverter connected with a bridge-rectifier RC load: (a) output voltage and current waveforms, (b) estimated parameter using the RLSE, (c) estimated parameter using the modified RLSE, and (d) estimated parameters of the averaging model using modified RLSE.



Fig. 6. Discrete adaptive repetitive controller.

where  $\hat{\theta}(0)$  is the initial guess of the parameters to be identified, P(k) is a positive definite measure of the estimation error, and its elements tend to decrease as the identification reaches its steady state. The scalar  $\alpha$  is a forgetting factor to weigh new data more heavily than old data. When  $\alpha = 1$ , all data are weighed equally. For  $0 < \alpha < 1$ , more weight is placed on recent data than on past data. A smaller  $\alpha$  will result in fast convergence, but the identified parameters will be more sensitive to measurement noise.

The PWM inverter used in an ac-voltage regulator is frequently connected to a bridge-rectifier RC load with its output waveforms as shown in Fig. 5(a). Fig. 5(b) shows the estimated parameters of (12) using RLSE, in which we can see that rapid convergence with large oscillation has occurred whenever the rectifier switches change their state. One way to avoid this phenomenon is that when the load transition is detected, a set of nominal parameters are used to set the RLSE parameters. Fig. 5(c) shows the estimated parameters of (12) using the modified RLSE.

Fig. 5(d) shows the estimated parameters of the averaging model using the modified RLSE method. The estimated parameters of the averaging model are derived using the following algorithms:

$$\overline{a}_{1}(n) = \frac{1}{N} \sum_{k=1}^{N} a'_{1}(k+nN)$$
(17)

$$\overline{a}_2(n) = \frac{1}{N} \sum_{k=1}^{N} a'_2(k+nN)$$
(18)

where N is the number of samples during one-half period of the output waveform. The auxiliary compensator  $S(z^{-1})$ of the repetitive controller is adjusted by using the estimated parameters of  $\overline{a}_1(n)$ ,  $\overline{a}_2(n)$  as shown in Fig. 6.



Fig. 7. Hardware configuration of the digital-controlled PWM inverter.

#### IV. MODELING AND CONTROL OF PWM INVERTER SYSTEM

The hardware configuration of the proposed DSP-based digital-controlled PWM inverter system is shown in Fig. 7, in which the combination of H-bridge PWM inverter, LC filter, and rectifier-type RC load is considered as the plant.

#### A. Plant Modeling

The capacitor voltage  $v_c$  and the inductor current  $i_L$  are chosen as the state variables and the system dynamic equations can be derived as

$$\dot{v}_c = \frac{Ri_L}{C(R+r_c)} - \frac{v_c}{C(R+r_c)}$$
 (19)

and

$$\dot{i}_L = -\left(\frac{r_L r_C + R(r_L + r_C)}{L(R + r_C)}\right) i_L - \left(\frac{R}{L(R + r_C)}\right) v_C + \frac{1}{L} v_i.$$
(20)

Since there are switching ripples in the capacitor voltage and inductor current, they are sensed through low-pass filters. Considering the dynamics of these filters, we can get

$$\dot{v}_1 = \frac{1}{R_1 C_1} i_L - \frac{1}{R_1 C_1} v_1 \tag{21}$$

$$\dot{v}_2 = \frac{1}{R_2 C_2} v_C - \frac{1}{R_2 C_2} v_2. \tag{22}$$

From (19) to (22), the state equation and output equation of the plant can be expressed as

$$\dot{x}(t) = \mathbf{A}x(t) + \mathbf{B}u(t) \tag{23}$$

$$\boldsymbol{J}(t) = \boldsymbol{C}\boldsymbol{X}(t) \tag{24}$$

$$x(t) = \begin{bmatrix} i_L(t) & v_C(t) & v_1(t) & v_2(t) \end{bmatrix}'$$
(25)

$$(t) = v_o(t) \tag{26}$$

$$u(t) = v_i(t) \tag{27}$$

and (28), given at the bottom of the page, as well as

$$\boldsymbol{B} = \begin{bmatrix} 1/L & 0 & 0 & 0 \end{bmatrix}$$
(29)

and

where

$$\boldsymbol{C} = \begin{bmatrix} \frac{Rr_C}{R+r_C} & \frac{R}{R+r_C} & 0 & 0 \end{bmatrix}.$$
 (30)

The corresponding discrete-time model can be derived as

$$x(k+1) = \boldsymbol{G}_p x(k) + \boldsymbol{H}_p u(k) \tag{31}$$

$$\mathbf{C}(k) = \mathbf{C}\mathbf{X}(k) \tag{32}$$

$$\boldsymbol{G}_{p} = e^{\boldsymbol{A}\boldsymbol{I}} \tag{33}$$

$$\boldsymbol{H}_p = \boldsymbol{A}^{-1} (\boldsymbol{e}^{\boldsymbol{A}\boldsymbol{I}} - \boldsymbol{I}) \boldsymbol{B}$$
(34)

$$x(k) = [i_L(k) \quad v_C(k) \quad v_1(k) \quad v_2(k)$$
 (35)

$$u(k) = v_i(k) \tag{36}$$

where T is the sampling period.

#### B. State Feedback Control

Considering the state feedback control block diagram in Fig. 8. The control law can be derived as

$$u(k) = k_0 v_{\rm ref}(k) - k_1 v_1(k) - k_2 v_2(k)$$
(37)

where  $v_{ref}(k)$  is a table of the reference command stored in the memory of the DSP. The state feedback gains  $k_0$ ,  $k_1$ ,

$$\boldsymbol{A} = \begin{bmatrix} -\frac{r_L r_C + R(r_L + r_C)}{L(R + r_C)} & -\frac{R}{L(R + r_C)} & & \\ & 0 & 0 & \\ \frac{R}{C(R + r_C)} & -\frac{1}{C(R + r_C)} & 0 & 0 \\ & \frac{1}{C(R + r_C)} & 0 & -\frac{1}{R_1 C_1} & 0 \\ & \frac{R r_C}{R_2 C_2 (R + r_C)} & \frac{R}{R_2 C_2 (R + r_C)} & 0 & -\frac{1}{R_2 C_2} \end{bmatrix}$$
(28)



Fig. 8. State feedback control block diagram of PWM inverter with nominal load  $R = 40 \ \Omega$ .

 TABLE I

 PARAMETERS OF THE PWM INVERTER SYSTEM

Item	Symbol	Nominal value	Unit
Sampling Rate	$f_s$	15k	Hz
Filter Inductor	L	0.6	mH
Filter Capacitor	С	3.3	μF
Inductor ESR	r <sub>L</sub>	0.5	Ω
Cap. serial resistor	r <sub>c</sub>	1.0	Ω
Nominal load	R	40	Ω
DC link voltage	V <sub>dc</sub>	300	Volts
Output voltage	v <sub>o</sub>	110	Volts (rms)
Switching frequency	$f_{sw}$	45k	Hz

and  $k_2$  can be determined by the method proposed in authors' previous research [22]. Combining (31), (32), and (37), we can obtain the state-space equation of the digital-controlled system as

$$x(k+1) = \boldsymbol{G}_c x(k) + \boldsymbol{H}_c v_{\text{ref}}(k)$$
(38)

$$y(k) = \boldsymbol{C}x(k) \tag{39}$$

where

$$\boldsymbol{G}_c = \boldsymbol{G}_p - \boldsymbol{H}_p \boldsymbol{K} \tag{40}$$

$$\boldsymbol{H}_c = k_0 \boldsymbol{H}_p \tag{41}$$

and

$$\boldsymbol{K} = \begin{bmatrix} 0 & 0 & k_1 & k_2 \end{bmatrix}. \tag{42}$$

The discrete-time transfer function from reference command to output voltage is

$$\frac{v_o(k)}{v_{\rm ref}(k)} = C(zI - G_c)^{-1}H_c = P_n(z^{-1}).$$
 (43)

It should be noted that the plant model  $P_n(z^{-1})$  is derived based on a nominal operating point, and in practical condition it will encounter large model uncertainties due to load variations.

#### C. Design Example

Table I lists some of the key parameters of the PWM inverter system used for 60-Hz 110-V (RMS) ac-voltage



Fig. 9. (a) Frequency responses of the state feedback-controlled PWM inverter system  $P_n(z^{-1})$  and the approximate model  $P_a(z^{-1})$ . (b) Nyquist plot of  $Q(z^{-1}) - 0.5P_n(z^{-1})P_a(z^{-1})$ , which shows stability of the repetitive control system when  $S(z^{-1}) = 1/P_a(z^{-1})$ .

regulation. A single-chip DSP TMS320C14 from Texas Instruments has been adopted to realize the proposed adaptive repetitive controller. The sampling frequency of the digital controller is 15 kHz and there are 250 samples in each cycle of the sinusoidal output. The tuning rate of the adaptive parameter



Fig. 10. (a) Simulation results of the error convergence using the proposed adaptive repetitive control scheme. (b) Time responses of the estimated parameters of the approximate average model.

tuner is 120 Hz, and it adjusts the control parameters of the repetitive controller in every half cycle.

The state feedback gain  $k_1$  and  $k_2$  are determined to minimize the output voltage distortion due to transient load disturbance and the feedforward gain  $k_0$  is a scaling factor to let the system have a unity gain at 60 Hz. With the given parameters as shown in Table I,  $k_1 = -0.9$  and  $k_2 = -0.45$ are selected as the state-feedback gains and the corresponding feedforward gain  $k_0$  is 0.56. The nominal transfer function  $P_n(z^{-1})$  of the digital state-feedback-controlled PWM inverter is

$$P_n(z^{-1}) = \frac{0.5310z^{-1} + 0.3655z^{-2} - 0.0485z^{-3} + 0.0003z^{-4}}{1 - 0.5182z^{-1} + 0.4789z^{-2} - 0.1273z^{-3} - 0.0015z^{-4}}.$$
(44)

The recursive LSE method has been used to identify the transfer function of the closed-loop-controlled PWM inverter based on a second-order model of (12). The identified plant model  $P_a(z^{-1})$  is

$$P_z(z^{-1}) = \frac{z^{-2}}{1 - 0.314z^{-1} + 0.296z^{-2}}.$$
 (45)

The frequency responses of  $P_a(z^{-1})$  and  $P_n(z^{-1})$  are shown in Fig. 9(a), and close resemblance between  $P_a(z^{-1})$  and



Fig. 11. (a) The 60-Hz-output waveforms for rectify load and current crest factor three with only state feedback control. (b) Output waveform when the adaptive repetitive control is applied.

 $P_n(z^{-1})$  can be observed. To assure enough stability margin, the scalar g is chosen as 0.5 and the auxiliary filter  $Q(z^{-1})$  is set as a constant gain of 0.95. The Nyquist plot of  $Q(z^{-1}) - P_n(z^{-1})P_a(z^{-1})$  is shown in Fig. 9(b), and it can be observed that it is within the stability boundary. This guarantees the stability of the repetitive control system.

#### V. SIMULATION AND EXPERIMENTAL VERIFICATION

Fig. 10 shows the simulation results of a DSP-controlled PWM inverter using the proposed adaptive repetitive control scheme for ac-voltage regulation. Fig. 10(a) shows a threedimension plot of the output voltage of the PWM inverter connected to bridge–rectifier load. The output waveforms in successive cycles after the adaptive repetitive controller applied are demonstrated in the same plot for convenience. The proposed control scheme does help to eliminate the periodic distortion and the output ac voltage becomes more sinusoidal after compensation. Fig. 10(b) shows the time responses of the estimated parameters of the second-order approximate, average model.

The experimental verification of the proposed adaptive repetitive control scheme is carried out on a 2-kVA PWM inverter connected to a rectifier RC load with current crest factor of three. Fig. 11(a) shows the experimental results of the output voltage and current of the PWM inverter using only the digital state feedback control. Fig. 11(b) shows the results under the same loading condition with the adaptive repetitive controller included. Fig. 12 shows the error of the



Fig. 12. Error convergence of the PWM inverter.

output ac voltage of the PWM inverter during the convergent process as the repetitive control is applied. The proposed control scheme does help to reduce the periodic error caused by cyclic disturbances. It can be observed that it takes about 12 cycles for the settling of the periodic error. For a 60-Hz output, it corresponds to 0.2 s to suppress the periodic disturbance caused by a step-changed rectifier RC load.

# VI. CONCLUSION

An adaptive repetitive control scheme is proposed and successfully applied to the closed-loop regulation of a PWM inverter used in a high-performance ac power supply. Simulation and experimental results show that the proposed control scheme can effectively eliminate periodic waveform distortion resulting from unknown cyclic disturbance. The repetitive controller helps to reduce the peak actuating force under cyclically changed load condition due to the adoption of the *a priori* information about the period of the disturbance. With the same dc-link voltage, the periodic error can be suppressed more easily than cases without repetitive compensation. As compared to conventional repetitive control methods, the proposed adaptive repetitive control scheme can not only achieve a faster convergence rate, but also guarantees stability robustness under large-load variations. The total harmonic distortion (THD) for the rectifier RC load with current crest factor of three can be reduced from 8% to 1% within 0.2 s after the proposed adaptive repetitive controller is applied. An important merit of the proposed adaptive repetitive control scheme is that it can be designed and implemented independently without knowing the exact model of the PWM inverter system. This shows the feasibility for inserting an adaptive repetitive control module into an analog-controlled PWM inverter to improve the quality of the output ac voltage.

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Ying-Yu Tzou (S'81–M'88) was born in Taiwan, R.O.C., on February 13, 1956. He received the B.S. and M.S. degrees in control engineering and the Ph.D. degree in electronics engineering from the Institute of Electronics Engineering, National Chiao Tung University, Hsinchu, Taiwan, in 1978, 1983, and 1987, respectively.

From 1980 to 1981, he was with the Electronic Research and Service Organization (ERSO) of the Industry Technology Research Institute (ITRI) as a Design Engineer in the Control System Department.

From 1983 to 1986, he was with Microtek Automation, Inc., as a Project Manager for the development of a computer numerical controller (CNC) for machine tools. He is currently a Professor in the Department of Electrical and Control Engineering, National Chiao Tung University, and also serves as an industrial consultant for many local power electronics and automation companies. He was the Director of the Institute of Control Engineering from 1992 to 1994. His special interests now are sensorless ac drives, intelligent UPS, FPGA-based control IC's for motor drives and power converters, real-time network control techniques, and DSP applications in power electronics and motion control.



Shih-Liang Jung (S'93) was born in Hsinchu, Taiwan, R.O.C., on September 15, 1969. He received the B.S. and Ph.D. degrees in control engineering from National Chiao Tung University, Hsinchu, in 1991 and 1996, respectively.

From 1996 to 1997, he was a Post-Doctoral Research Fellow at National Chiao Tung University. He is with the army for military duty and has served as an Instructor at the training center of Military Integral Communication Command (MICC) since July 1997. His research interests include variable

structure systems, DSP-based digital control techniques, control of power electronic systems, and microwave communication systems.



Hsin-Chung Yeh was born in Taipei, Taiwan, R.O.C., on December 29, 1970. He received the B.S. and M.S. degrees in control engineering from the Department of Control Engineering, National Chiao Tung University, Hsinchu, Taiwan, in 1993 and 1995, respectively.

From 1995 to 1997, he was with the army for military service. On July 1997, he joined ACER as a Servo Engineer for the development of a DVD ROM system. Currently, he is with Afreey, Inc., Taipei, an optical data storage system company,

where he engages in the design of a high-speed DVD ROM. His research interests include DSP-based digital control, control of power electronic systems, and optical data storage systems.